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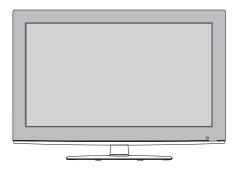
## LED LCD TV SERVICE MANUAL

**CHASSIS: LJ01U** 

MODEL: 32LV2500 32LV2500-SA

## **CAUTION**

BEFORE SERVICING THE CHASSIS,
READ THE SAFETY PRECAUTIONS IN THIS MANUAL.



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## SAFETY PRECAUTIONS

## **IMPORTANT SAFETY NOTICE**

Many electrical and mechanical parts in this chassis have special safety-related characteristics. These parts are identified by  $ilde{\Lambda}$  in the Schematic Diagram and Exploded View.

It is essential that these special safety parts should be replaced with the same components as recommended in this manual to prevent Shock, Fire, or other Hazards.

Do not modify the original design without permission of manufacturer.

#### **General Guidance**

An **isolation Transformer should always be used** during the servicing of a receiver whose chassis is not isolated from the AC power line. Use a transformer of adequate power rating as this protects the technician from accidents resulting in personal injury from electrical shocks.

It will also protect the receiver and it's components from being damaged by accidental shorts of the circuitry that may be inadvertently introduced during the service operation.

If any fuse (or Fusible Resistor) in this TV receiver is blown, replace it with the specified.

When replacing a high wattage resistor (Oxide Metal Film Resistor, over 1W), keep the resistor 10mm away from PCB.

Keep wires away from high voltage or high temperature parts.

## Before returning the receiver to the customer,

always perform an **AC leakage current check** on the exposed metallic parts of the cabinet, such as antennas, terminals, etc., to be sure the set is safe to operate without damage of electrical shock.

#### Leakage Current Cold Check(Antenna Cold Check)

With the instrument AC plug removed from AC source, connect an electrical jumper across the two AC plug prongs. Place the AC switch in the on position, connect one lead of ohm-meter to the AC plug prongs tied together and touch other ohm-meter lead in turn to each exposed metallic parts such as antenna terminals, phone jacks, etc.

If the exposed metallic part has a return path to the chassis, the measured resistance should be between 1M $\Omega$  and 5.2M $\Omega$ .

When the exposed metal has no return path to the chassis the reading must be infinite.

An other abnormality exists that must be corrected before the receiver is returned to the customer.

## Leakage Current Hot Check (See below Figure)

Plug the AC cord directly into the AC outlet.

### Do not use a line Isolation Transformer during this check.

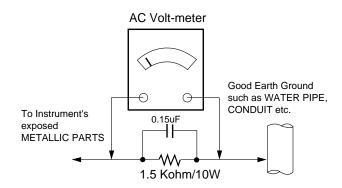
Connect 1.5K/10watt resistor in parallel with a 0.15uF capacitor between a known good earth ground (Water Pipe, Conduit, etc.) and the exposed metallic parts.

Measure the AC voltage across the resistor using AC voltmeter with 1000 ohms/volt or more sensitivity.

Reverse plug the AC cord into the AC outlet and repeat AC voltage measurements for each exposed metallic part. Any voltage measured must not exceed 0.75 volt RMS which is corresponds to 0.5mA.

In case any measurement is out of the limits specified, there is possibility of shock hazard and the set must be checked and repaired before it is returned to the customer.

## Leakage Current Hot Check circuit



## SERVICING PRECAUTIONS

CAUTION: Before servicing receivers covered by this service manual and its supplements and addenda, read and follow the SAFETY PRECAUTIONS on page 3 of this publication.

*NOTE:* If unforeseen circumstances create conflict between the following servicing precautions and any of the safety precautions on page 3 of this publication, always follow the safety precautions. Remember: Safety First.

### **General Servicing Precautions**

- Always unplug the receiver AC power cord from the AC power source before;
  - Removing or reinstalling any component, circuit board module or any other receiver assembly.
  - Disconnecting or reconnecting any receiver electrical plug or other electrical connection.
  - Connecting a test substitute in parallel with an electrolytic capacitor in the receiver.
    - **CAUTION:** A wrong part substitution or incorrect polarity installation of electrolytic capacitors may result in an explosion hazard.
- Test high voltage only by measuring it with an appropriate high voltage meter or other voltage measuring device (DVM, FETVOM, etc) equipped with a suitable high voltage probe.
   Do not test high voltage by "drawing an arc".
- Do not spray chemicals on or near this receiver or any of its assemblies
- 4. Unless specified otherwise in this service manual, clean electrical contacts only by applying the following mixture to the contacts with a pipe cleaner, cotton-tipped stick or comparable non-abrasive applicator; 10% (by volume) Acetone and 90% (by volume) isopropyl alcohol (90%-99% strength)

**CAUTION:** This is a flammable mixture.

- Unless specified otherwise in this service manual, lubrication of contacts in not required.
- 5. Do not defeat any plug/socket B+ voltage interlocks with which receivers covered by this service manual might be equipped.
- Do not apply AC power to this instrument and/or any of its electrical assemblies unless all solid-state device heat sinks are correctly installed.
- Always connect the test receiver ground lead to the receiver chassis ground before connecting the test receiver positive lead.
  - Always remove the test receiver ground lead last.
- Use with this receiver only the test fixtures specified in this service manual.

**CAUTION:** Do not connect the test fixture ground strap to any heat sink in this receiver.

## **Electrostatically Sensitive (ES) Devices**

Some semiconductor (solid-state) devices can be damaged easily by static electricity. Such components commonly are called *Electrostatically Sensitive (ES) Devices*. Examples of typical ES devices are integrated circuits and some field-effect transistors and semiconductor "chip" components. The following techniques should be used to help reduce the incidence of component damage caused by static by static electricity.

 Immediately before handling any semiconductor component or semiconductor-equipped assembly, drain off any electrostatic charge on your body by touching a known earth ground. Alternatively, obtain and wear a commercially available discharging wrist strap device, which should be removed to prevent potential shock reasons prior to applying power to the unit under test.

- After removing an electrical assembly equipped with ES devices, place the assembly on a conductive surface such as aluminum foil, to prevent electrostatic charge buildup or exposure of the assembly.
- Use only a grounded-tip soldering iron to solder or unsolder ES
  devices
- Use only an anti-static type solder removal device. Some solder removal devices not classified as "anti-static" can generate electrical charges sufficient to damage ES devices.
- 5. Do not use freon-propelled chemicals. These can generate electrical charges sufficient to damage ES devices.
- 6. Do not remove a replacement ES device from its protective package until immediately before you are ready to install it. (Most replacement ES devices are packaged with leads electrically shorted together by conductive foam, aluminum foil or comparable conductive material).
- Immediately before removing the protective material from the leads of a replacement ES device, touch the protective material to the chassis or circuit assembly into which the device will be installed.

**CAUTION:** Be sure no power is applied to the chassis or circuit, and observe all other safety precautions.

 Minimize bodily motions when handling unpackaged replacement ES devices. (Otherwise harmless motion such as the brushing together of your clothes fabric or the lifting of your foot from a carpeted floor can generate static electricity sufficient to damage an ES device.)

#### General Soldering Guidelines

- Use a grounded-tip, low-wattage soldering iron and appropriate tip size and shape that will maintain tip temperature within the range or 500°F to 600°F.
- Use an appropriate gauge of RMA resin-core solder composed of 60 parts tin/40 parts lead.
- 3. Keep the soldering iron tip clean and well tinned.
- Thoroughly clean the surfaces to be soldered. Use a mall wirebristle (0.5 inch, or 1.25cm) brush with a metal handle.
   Do not use freon-propelled spray-on cleaners.
- 5. Use the following unsoldering technique
  - a. Allow the soldering iron tip to reach normal temperature. ( $500^{\circ}\text{F}$  to  $600^{\circ}\text{F}$ )
  - b. Heat the component lead until the solder melts.
  - Quickly draw the melted solder with an anti-static, suctiontype solder removal device or with solder braid.
     CAUTION: Work quickly to avoid overheating the circuit board printed foil.
- 6. Use the following soldering technique.
  - a. Allow the soldering iron tip to reach a normal temperature (500°F to 600°F)
  - b. First, hold the soldering iron tip and solder the strand against the component lead until the solder melts.
  - c. Quickly move the soldering iron tip to the junction of the component lead and the printed circuit foil, and hold it there only until the solder flows onto and around both the component lead and the foil.
    - **CAUTION:** Work quickly to avoid overheating the circuit board printed foil.
  - d. Closely inspect the solder area and remove any excess or splashed solder with a small wire-bristle brush.

### IC Remove/Replacement

Some chassis circuit boards have slotted holes (oblong) through which the IC leads are inserted and then bent flat against the circuit foil. When holes are the slotted type, the following technique should be used to remove and replace the IC. When working with boards using the familiar round hole, use the standard technique as outlined in paragraphs 5 and 6 above.

#### Removal

- Desolder and straighten each IC lead in one operation by gently prying up on the lead with the soldering iron tip as the solder melts.
- Draw away the melted solder with an anti-static suction-type solder removal device (or with solder braid) before removing the IC.

#### Replacement

- 1. Carefully insert the replacement IC in the circuit board.
- Carefully bend each IC lead against the circuit foil pad and solder it.
- Clean the soldered areas with a small wire-bristle brush. (It is not necessary to reapply acrylic coating to the areas).

## "Small-Signal" Discrete Transistor

#### Removal/Replacement

- 1. Remove the defective transistor by clipping its leads as close as possible to the component body.
- Bend into a "U" shape the end of each of three leads remaining on the circuit board.
- 3. Bend into a "U" shape the replacement transistor leads.
- 4. Connect the replacement transistor leads to the corresponding leads extending from the circuit board and crimp the "U" with long nose pliers to insure metal to metal contact then solder each connection.

## Power Output, Transistor Device Removal/Replacement

- 1. Heat and remove all solder from around the transistor leads.
- 2. Remove the heat sink mounting screw (if so equipped).
- Carefully remove the transistor from the heat sink of the circuit board.
- 4. Insert new transistor in the circuit board.
- 5. Solder each transistor lead, and clip off excess lead.
- 6. Replace heat sink.

#### **Diode Removal/Replacement**

- Remove defective diode by clipping its leads as close as possible to diode body.
- Bend the two remaining leads perpendicular y to the circuit board.
- Observing diode polarity, wrap each lead of the new diode around the corresponding lead on the circuit board.
- 4. Securely crimp each connection and solder it.
- Inspect (on the circuit board copper side) the solder joints of the two "original" leads. If they are not shiny, reheat them and if necessary, apply additional solder.

#### **Fuse and Conventional Resistor**

#### Removal/Replacement

- Clip each fuse or resistor lead at top of the circuit board hollow stake.
- Securely crimp the leads of replacement component around notch at stake top.
- 3. Solder the connections.

**CAUTION:** Maintain original spacing between the replaced component and adjacent components and the circuit board to prevent excessive component temperatures.

#### Circuit Board Foil Repair

Excessive heat applied to the copper foil of any printed circuit board will weaken the adhesive that bonds the foil to the circuit board causing the foil to separate from or "lift-off" the board. The following guidelines and procedures should be followed whenever this condition is encountered.

#### At IC Connections

To repair a defective copper pattern at IC connections use the following procedure to install a jumper wire on the copper pattern side of the circuit board. (Use this technique only on IC connections).

- Carefully remove the damaged copper pattern with a sharp knife. (Remove only as much copper as absolutely necessary).
- carefully scratch away the solder resist and acrylic coating (if used) from the end of the remaining copper pattern.
- 3. Bend a small "U" in one end of a small gauge jumper wire and carefully crimp it around the IC pin. Solder the IC connection.
- 4. Route the jumper wire along the path of the out-away copper pattern and let it overlap the previously scraped end of the good copper pattern. Solder the overlapped area and clip off any excess jumper wire.

#### At Other Connections

Use the following technique to repair the defective copper pattern at connections other than IC Pins. This technique involves the installation of a jumper wire on the component side of the circuit board.

- Remove the defective copper pattern with a sharp knife.
   Remove at least 1/4 inch of copper, to ensure that a hazardous condition will not exist if the jumper wire opens.
- Trace along the copper pattern from both sides of the pattern break and locate the nearest component that is directly connected to the affected copper pattern.
- Connect insulated 20-gauge jumper wire from the lead of the nearest component on one side of the pattern break to the lead of the nearest component on the other side.

Carefully crimp and solder the connections.

**CAUTION:** Be sure the insulated jumper wire is dressed so the it does not touch components or sharp edges.

## **SPECIFICATION**

NOTE: Specifications and others are subject to change without notice for improvement.

## 1. Application range

This spec sheet is applied LCD TV with LJ01U chassis.

## 2. Requirement for Test

Each part is tested as below without special appointment.

- 1) Temperature:  $25 \, ^{\circ}\text{C} \pm 5 \, ^{\circ}\text{C}$ 2) Relative Humidity:  $65 \pm 10 \, \%$
- 3) Power Voltage: Standard input voltage(100-240V~, 50/60Hz)
  \* Standard Voltage of each product is marked by models
- Specification and performance of each parts are followed each drawing and specification by part number in accordance with BOM.
- 5) The receiver must be operated for about 5 minutes prior to the adjustment.

## 3. Test method

- 1) Performance: LGE TV test method followed
- 2) Demanded other specification
  - Safety : UL, CSA, IEC specification
  - EMC: FCC, ICES, IEC specification

## 4. General Specification(TV)

No	Item	Specification			Remark
1	Receivable System	1) ATSC / NTSC-M			
2	Available Channel	1) VHF : 02 ~ 13			
		2) UHF : 14 ~ 69			
		3) DTV : 07 ~ 69			
		4) CATV : 01 ~ 135			
3	Input Voltage	1) AC 100 - 240V~ 50/60Hz			
4	Market	Central and South AMERICA			
5	Screen Size	26 inch Wide (1366 x 768)			26LK330
		32 inch Wide (1366 x 768)			32LV2500, 32LK330
		32 inch Wide (1920 x 1080)			32LV4500, 32LV3500, 32LV3400, 32LK450, 32LK430
		37 inch Wide (1920 x 1080)			37LV3500, 37LK450
		42 inch Wide (1920 x 1080)			42LV4500, 42LV3500, 42LV3400, 42LK550, 42LK450
		47 inch Wide (1920 x 1080)			47LV3500, 47LK450
		55 inch Wide (1920 x 1080)			55LV3500
6	Aspect Ratio	16:9			
7	Tuning System	FS			
8	LCD Module	T260XW04-V9	HD, 60Hz	AUO	26LK330
		T315XW03-VF	HD, 60Hz	AUO	32LK330
		LC320WXN-SCA2		LGD	
		LC320WXE-SCA1		LGD	
		T315HW04-V9	FHD, 60Hz	AUO	32LK430
		T315HW04-V9	FHD, 60Hz	AUO	32LK450
		LC320WUN-SDA1		LGD	
		LC320WUE-BCA1		LGD	
		T315XW06-V3	HD, 60Hz	AUO	32LV2500
		LC320EXN-SDA1		LGD	
		VVX32H110G00		IPS	
		LC320EUN-SDV2	FHD, 60Hz	LGD	32LV3400
		V315H3-LE7	FHD, 60Hz	CMI	32LV3500
		T315HW07-V8		AUO	
		LC320EUN-SDV2		LGD	
		LC320EUD-SDA1			32LV4500
		LC370WUE-SCA1	FHD, 60Hz	LGD	37LK450
		T370HW05-V1	FHD, 60Hz	LGD	37LV3500
		LC370EUN-SDV2		AUO	
		T420HW09-V0	FHD, 60Hz	AUO	42LK450
		LC420WUE-SCA2		LGD	
		LC420WUF-SCA2	FHD, 120Hz	LGD	42LK550
		LC420EUN-SDV3	FHD, 60Hz		42LV3400, 42LV3500
		T420HW08-V1	FHD, 60Hz	AUO	42LV3500
		LC420EUF-SDA1	FHD, 120Hz	LGD	42LV4500
		LC470WUE-SCA2	FHD, 60Hz		42LK450
		LC470EUE-SDV1	FHD, 60Hz		47LV3500
		LC550EUF-SDA1	FHD, 120Hz	LGD	55LV3500
9	Operating Environment	Temp : 0 ~ 40 deg			
		Humidity: ~ 80 %			
10	Storage Environment	Temp : -20 ~ 60 deg			
		Humidity: -85 %			

## 5. Chrominance & Luminance(32LV2500-SA)

No	Ite	em		Min	Тур	Max	Unit	Remark
1	Max Luminance		Module	300	380		cd/m²	LGD
	(Center 1-point /			280	350		cd/m²	AUO
	Full White Pattern)			250	350		cd/m²	IPS
			SET	260	340		cd/m²	LGD
				240	310		cd/m²	AUO
				220	310		cd/m²	IPS
2	Luminance uniformity							
3	Color coordinate	RED	Х	Тур.	0.637	Тур.		LGD
				-0.03	0.640	+0.03		AUO
			1		0.645			IPS
			Υ		0.341			LGD
					0.330			AUO
					0.330			IPS
		GREEN	Х		0.318			LGD
					0.310			AUO
					0.300			IPS
			Υ		0.606			LGD
					0.620			AUO
					0.620			IPS
		BLUE	Х		0.153			LGD
					0.150			AUO
					0.153			IPS
			Υ		0.057			LGD
					0.060			AUO
					0.065			IPS
		WHITE	Х		0.279			LGD
					0.280			AUO
					0.278			IPS
			Υ		0.292			LGD
					0.290			AUO
					0.285			IPS
3.	Contrast ratio	•		850	1200			LGD
				2400	3000			AUO
				700	1200			IPS
4.	Color Temperature	Cool		0.254	0.269	0.284		<test condition=""></test>
				0.258	0.273	0.288		* The W/B Tolerance is ± 0.015 for
		Standard		0.270	0.285	0.300		Adjustment
				0.278	0.293	0.308		
		Warm		0.298	0.313	0.324		
				0.314	0.329	0.344		

## 6. Component Video Input (Y, CB/PB, CR/PR)

No	Resolution	H-freq(kHz)	V-freq.(kHz)	Pixel clock	Proposed
1.	720*576	15.625	50.000	13.5	SDTV 576I
2.	720*480	15.73	60	13.5135	SDTV ,DVD 480I
3.	720*480	15.73	59.94	13.5	SDTV ,DVD 480I
4.	720*480	31.50	60	27.027	SDTV 480P
5.	720*480	31.47	59.94	27.0	SDTV 480P
6.	720*576	31.250	50.000	27.000	SDTV 576P
7.	1280*720	37.500	50.000	74.25	HDTV 720P
8.	1280*720	45.00	60.00	74.25	HDTV 720P
9.	1280*720	44.96	59.94	74.176	HDTV 720P
10.	1920*1080	28.125	50.00	74.250	HDTV 1080I
11.	1920*1080	33.75	60.00	74.25	HDTV 1080I
12.	1920*1080	33.72	59.94	74.176	HDTV 1080I
13.	1920*1080	56.250	50.00	148.50	HDTV 1080P
14.	1920*1080	67.500	60	148.50	HDTV 1080P
15.	1920*1080	67.432	59.94	148.352	HDTV 1080P
16.	1920*1080	27.000	24.000	74.25	HDTV 1080P
17.	1920*1080	26.97	23.976	74.176	HDTV 1080P
18.	1920*1080	33.75	30.000	74.25	HDTV 1080P
19.	1920*1080	33.71	29.97	740176	HDTV 1080P

## 7. RGB Input (PC)

No	Resolution	H-freq(kHz)	V-freq.(kHz)	Pixel clock	Propose	d
	PC				DDC	
1.	640*350	31.468	70.09	25.17	EGA	Х
2.	720*400	31.469	70.08	28.32	DOS	0
3.	640*480	31.469	59.94	25.17	VESA(VGA)	0
4.	800*600	37.879	60.31	40.00	VESA(SVGA)	0
5.	1024*768	48.363	60.00	65.00	VESA(XGA)	0
6.	1280*768	47.78	59.870	79.5	CVT(WXGA)	0
7.	1360*768	47.712	60.015	85.50	VESA(WXGA)	0
8.	1280*1024	63.981	60.020	108.00	VESA(SXGA)	0
9.	1920*1080	67.5	60	148.5	HDTV 1080P	0

•RGB PC Monitor Range Limits Min Vertical Freq - 58 Hz Max Vertical Freq - 62 Hz Min Horiz. Freq - 30 kHz Max Horiz. Freq - 83 kHz Pixel Clock - 160 MHz

## 8. HDMI input (PC/DTV)

No	Resolution	H-freq(kHz)	V-freq.(kHz)	Pixel clock	Proposed	
	PC				DDC	
1.	640*350	31.468	70.09	25.17	EGA	Х
2.	720*400	31.469	70.08	28.32	DOS	0
3.	640*480	31.469	59.94	25.17	VESA(VGA)	0
4.	800*600	37.879	60.31	40.00	VESA(SVGA)	0
5.	1024*768	48.363	60.00	65.00	VESA(XGA)	0
6.	1360*768	47.712	60.015	85.50	VESA (WXGA)	0
7.	1280*1024	63.981	60.020	108.00	VESA (SXGA)	0
8.	1920*1080	67.500	60.000	148.50	HDTV 1080P	0
	DTV					
1	720*480	31.469	59.940	27.000	SDTV 480P	
2	720*480	31.500	60	27.027	SDTV 480P	
3	720*576	31.250	50.000	27.000	SDTV 576P	
4	1280*720	37.500	50.000	74.25	HDTV 720P	
5	1280*720	45.00	60.00	74.25	HDTV 720P	
6	1280*720	44.96	59.94	74.176	HDTV 720P	
7	1920*1080	28.125	50.000	74.25	HDTV 1080I	
8	1920*1080	33.75	60.00	74.25	HDTV 1080I	
9	1920*1080	33.72	59.94	74.176	HDTV 1080I	
10	1920*1080	56.250	50.000	148.50	HDTV 1080P	
11	1920*1080	67.500	60	148.50	HDTV 1080P	
12	1920*1080	67.432	59.94	148.352	HDTV 1080P	
13	1920*1080	27.000	24.000	74.25	HDTV 1080P	
14	1920*1080	26.97	23.976	74.176	HDTV 1080P	
15	1920*1080	33.75	30.000	74.25	HDTV 1080P	
16	1920*1080	33.71	29.97	74.176	HDTV 1080P	

•HDMI Monitor Range Limits Min Vertical Freq - 58 Hz Min Horiz. Freq - 30 kHz Pixel Clock - 160 MHz

Max Vertical Freq - 62 Hz Max Horiz. Freq - 83 kHz

## **ADJUSTMENT INSTRUCTION**

## 1. Application Range

This specification sheet is applied to all of the LCD TV with LJ01U/LJ01T/LJ01S chassis.

## 2. Designation

- The adjustment is according to the order which is designated and which must be followed, according to the plan which can be changed only on agreeing.
- 2) Power Adjustment: Free Voltage
- 3) Magnetic Field Condition: Nil.
- 4) Input signal Unit: Product Specification Standard
- 5) Reserve after operation: Above 5 Minutes (Heat Run) Temperature : at 25 °C ± 5 °C

Relative humidity: 65 % ± 10 % Input voltage: 100 ~ 220 V~, 50/60 Hz

- Adjustment equipments: Color Analyzer(CA-210 or CA-110), DDC Adjustment Jig equipment, Service remote control.
- 7) Push the "IN STOP" key For memory initialization.

Case1: Software version up

- After downloading S/W by USB, TV set will reboot automatically
- 2. Push "In-stop" key
- 3. Push "Power on" key
- 4. Function inspection
- 5. After function inspection, Push "In-stop" key.

Case2: Function check at the assembly line

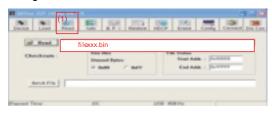
- 1. When TV set is entering on the assembly line, Push "In-stop" key at first.
- 2. Push "Power on" key for turning it on.
  - -> If you push "Power on" key, TV set will recover channel information by itself.
- 3. After function inspection, Push "In-stop" key.

## 3. Main PCB check process

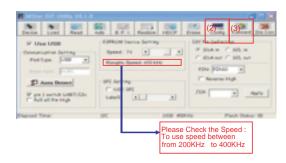
\* APC - After Manual-Insult, executing APC

## \* Boot file Download

1) Execute ISP program "Mstar ISP Utility" and then click "Config" tab.



- Set as below, and then click "Auto Detect" and check "OK" message
  - If "Error" is displayed, Check connection between computer, jig, and set.
- 3) Click "Read" tab, and then load download file (XXXX.bin) by clicking "Read"
- 4) Click "Connect" tab. If "Can't" is displayed, check connection between computer, jig, and set.

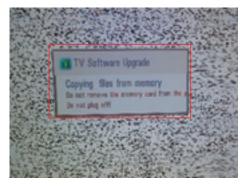


- 5) Click "Auto" tab and set as below
- 6) Click "Run".
- 7) After downloading, check "OK" message.



#### \* USB DOWNLOAD

- 1) Put the USB Stick to the USB socket
- 2) Automatically detecting update file in USB Stick
  - If your downloaded program version in USB Stick is Low, it didn't work. But your downloaded version is High, USB data is automatically detecting
- 3) Show the message "Copying files from memory"



4) Updating is staring





- 5) Uploading completed, The TV will restart automatically.
- 6) If your TV is turned on, check your updated version and Tool option.(explain the Tool option, next stage)
  - \* If downloading version is more high than your TV have, TV can lost all channel data. In this case, you have to channel recover. if all channel data is cleared, you didn't have a DTV/ATV test on production line.

## \* After downloading, have to adjust Tool Option again.

- 1) Push "IN-START" key in service remote control.
- 2) Select "Tool Option 1" and Push "OK" button.
- 3) Punch in the number. (Each model has their number)
- 4) Completed selecting Tool option.

Modu le	Module	Too I option1	Too I option2	Too I option3	Too I option4	Too I option5
42LV4500-SA	LGD	26496	19478	55337	10520	8448
32LV4500-SA	LGD	18304	19478	55337	10520	8480
55LV3500-SA	LGD	46944	19478	55337	10520	8448
47LV3500-SA	LGD	34656	19478	55337	10520	256
42LV3500-SA	AUO	26472	19478	55337	10520	352
42LV3500-SA	LGD	26464	19478	55337	10520	288
37LV3500-SA	AUO	22376	19478	55337	10520	352
37LV3500-SA	LGD	22368	19478	55337	10520	288
32LV3500-SA	CMI	18276	19478	55337	10520	352
32LV3500-SA	AUO	18280	19478	55337	10520	352
32LV3500-SA	LGD	18272	19478	55337	10520	288
42LV3400-SA	LGD	26592	9226	53289	10520	288
32LV3400-SA	LGD	18400	9226	53289	10520	288
32LV2500-SA	CMI	18212	19478	55337	10520	352
32LV2500-SA	AUO	18216	19478	55337	10520	352
32LV2500-SA	LGD	18208	19478	55337	10520	288
26LV2500-SA	AUO	14120	8714	55337	10520	288
26LV2500-SA	LGD	14112	8714	55337	10520	288
22LV2500-SA	CMI	10020	8714	55337	10520	288
22LV2500-SA	AUO	10024	8714	55337	10520	288
19LV2500-SA	AUO	5928	8714	55337	10520	288
19LV2500-SA	CMI	5924	8714	55337	10520	288
19LV2500-SA	LGD	5920	8714	55337	10520	288
47LK450-SA	LGD	34432	18966	55305	10520	8450
42LK450-SA	LGD	26240	18966	55305	10520	8450
42LK450-SA	AUO	26248	18966	55305	10520	290
37LK450-SA	LGD	22144	18966	55305	10520	8450
37LK450-SA	AUO	22152	18966	55305	10520	290
32LK450-SA	AUO	18056	18966	55305	10520	290
32LK450-SA	LGD	18048	18966	55305	10520	8482
32LK450-SA	LGD (CoMS)	18048	18966	55305	10520	8450
32LK430-SA	AUO	18024	18966	51209	10520	290
32LK330-SB	AUO	17992	18966	51209	10520	290
32LK330-SB	LGD	17984	18966	51209	10520	290
32LK330-SB	LGD (CoMS)	17984	18966	51209	10520	258
26LK330-SB	AUO	13896	18966	51209	10520	290

## 3.1. ADC Process

- (1) ADC
  - Enter Service Mode by pushing "ADJ" key,
  - Enter Internal ADC mode by pushing " $_{\rm G}$ " key at "6. ADC Calibration"



<Caution> Using 'power on' button of the Adjustment R/C, power on TV.

#### \* ADC Calibration Protocol (RS232)

No	Item	CMD1	CMD2	Da	ta0	
Enter Adjust	Adjust	Α	Α	0	0	When transfer the 'Mode In',
Mode	'Mode In'					Carry the command.
ADC adjust	ADC Adjust	Α	D	1	0	Automatically adjustment
						(The use of a internal pattern)

## Adjust Sequence

- aa 00 00 [Enter Adjust Mode]
- xb 00 40 [Component1 Input (480i)]
- ad 00 10 [Adjust 480i Comp1]
- xb 00 60 [RGB Input (1024\*768)]
- ad 00 10 [Adjust 1024\*768 RGB]
- aa 00 90 End Adjust mode
- \* Required equipment : Adjustment R/C.

### 3.2. Function Check

- \* Check display and sound
  - Check Input and Signal items. (cf. work instructions)
  - 1) TV
  - 2) AV (SCART1/SCART2/ CVBS)
  - 3) COMPONENT (480i)
  - 4) RGB (PC: 1024 x 768 @ 60hz)
  - 5) HDMI
  - 6) PC Audio In
  - \* Display and Sound check is executed by Remote control.

## 4. Total Assembly line process

## 4.1. Adjustment Preparation

· W/B Equipment condition

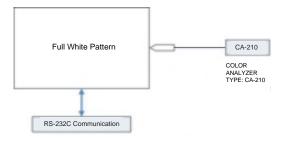
CA210: CH 9. Test signal: Inner pattern (85IRE)

· Above 5 minutes H/run in the inner pattern. ("power on" key of adjust remote control)

Cool	13,000	K	X=0.269(±0.002)	
			Y=0.273(±0.002)	<test signal=""></test>
Medium	9,300	K	X=0.285(±0.002)	Inner pattern
			Y=0.293(±0.002)	(216gray,85IRE)
Warm	6,500	K	X=0.313(±0.002)	
			Y=0.329(±0.002)	

\* Connecting picture of the measuring instrument (On Automatic control)

Inside PATTERN is used when W/B is controlled. Connect to auto controller or push Adjustment R/C POWER ON -> Enter the mode of White-Balance, the pattern will come out.



- \* Auto-control interface and directions
- 1) Adjust in the place where the influx of light like floodlight around is blocked. (illumination is less than 10 lux).
- 2) Adhere closely the Color Analyzer (CA210) to the module less than 10 cm distance, keep it with the surface of the Module and Color Analyzer's prove vertically.(80° ~ 100°).
- 3) Aging time
  - After aging start, keep the power on (no suspension of power supply) and heat-run over 5 minutes.
  - Using 'no signal' or 'full white pattern' or the others, check the back light on.
- Auto adjustment Map(RS-232C) RS-232C COMMAND

[CMD ID DATA]

Wb 00 00 00

Wb

White Balance Start White Balance End

	RS-232	C COM	MAND	MIN	C	CENTER			
	[CI	MD ID E	DATA]		(DEFAULT)				
	Cool	Mid	Warm		Cool	Mid	Warm		
R Gain	jg	Ja	jd	00	172	192	192	192	
G Gain	jh	Jb	je	00	172	192	192	192	
B Gain	ji	Jc	jf	00	192	192	172	192	
R Cut					64	64	64	128	
G Cut					64	64	64	128	
B Cut					64	64	64	128	

<Caution>

Color Temperature: COOL, Medium, Warm.

One of R Gain/G Gain/ B Gain should be kept on 0xC0, and adjust other two lower than C0.

(when R/G/B Gain are all C0, it is the FULL Dynamic Range of Module)

- \* Manual W/B process using adjusts Remote control.
- After enter Service Mode by pushing "ADJ" key,
- Enter White Balance by pushing "G" key at "6. White Balance".





- \* After done all adjustments, Press "In-start" button and compare Tool option and Area option value with its BOM, if it is correctly same then unplug the AC cable. If it is not same, then correct it same with BOM and unplug AC cable. For correct it to the model's module from factory Jig model.
- \* Push the "IN STOP" key after completing the function inspection. And Mechanical Power Switch must be set "ON".

## 4.2. DDC EDID Write (RGB 128Byte)

- Connect D-sub Signal Cable to D-sub Jack.
- Write EDID Data to EEPROM(24C02) by using DDC2B protocol.
- Check whether written EDID data is correct or not.
- \* For SVC main Assembly, EDID have to be downloaded to Insert Process in advance.

## 4.3. DDC EDID Write (HDMI 256Byte)

- Connect HDMI Signal Cable to HDMI Jack.
- Write EDID Data to EEPROM(24C02) by using DDC2B protocol.
- Check whether written EDID data is correct or not.
- \* For SVC main Assembly, EDID have to be downloaded to Insert Process in advance.

### 4.4. EDID DATA

1) All Data: HEXA Value

2) Changeable Data:

\*: Serial No : Controlled / Data:01

\*\*: Month: Controlled / Data:00

\*\*\*:Year : Controlled \*\*\*\*:Check sum

## 4.5. Auto Download

- 1) Press Adj. key on the Adj. Remote control.
- 2) Select EDID D/L menu.
- 3) By pressing Enter key, EDID download will begin
- 4) If Download is successful, OK is display, but If Download if failure, NG is displayed.
- 5) If Download is failure, Re-try downloads.





### \* Edid data and Model option download (RS232)

NO	Item	CMD1	CMD2	D	ata0	
Enter	Download	Α	Α	0	0	When transfer the 'Mode In',
download Mode	'Mode In'					Carry the command.
EDID data	Download	Α	Е	00	10	Automatically Download
and Model						(The use of a internal pattern)
option download						

## - Manual Download

- \* Caution
- 1) Use the proper signal cable for EDID Download
  - Analog EDID : Pin3 exists
  - Digital EDID : Pin3 exists
- 2) Never connect HDMI & D-sub Cable at the same time.
- 3) Use the proper cables below for EDID Writing
- 4) Download HDMI1, HDMI2, separately because HDMI1 is different from HDMI2

For Analog EDID	For HDMI EDID				
D-sub to D-sub	DVI-D to HDMI or HDMI to HDMI				
	P				

Item	Condition	Data(Hex)
Manufacturer ID	GSM	1E6D
Version	Digital : 1	01
Revision	Digital : 3	03

## 1) FHD RGB EDID data(Check sum:1C)

Addr	00	01	02	03	04	05	06	07	08	09	0.4	08	0C	0D	0E	0F
0000																
0010	01	15	01	03	68	10	09	78	40	EE	91	A3	54	4C	99	26
0020	0F	50	54	A1	08	00	81	80	61	40	45	40	31	40	01	01
0030																
0040																
0050	6E	28	5.5	00	A0	54	00	0.0	00	1E	0.0	00	00	FD	00	3A
0060	3E	1E	5.3	10	00	WO	20	20	20	20	20	20	00	0.0	00	FC
0070	00	4C	47	20	54	56	OA	20	20	20	2:0	20	20	2:0	00	10

### 2) FHD HDMI EDID data HDMI 1 (Check sum:E2, 99)

Addr	00	01	02	03	04	05	06	07	08	09	0A	0B	0C	0D	0E	0E
0000	00			FF												01
0010	01	15	01	03	80	10	09	78	OA.	EE	91	A3	54	4C	99	26
0020	0F	50		A1										01		01
0030	01	01	01	01	01	01	0.2	ЭA	80	18	71	38	2D	40	50	20
0040	45	00	AO	5A	0.0	00	0.0	1E	1B	21	50	AO	51	00	1E	30
0050	48	88	35	0.0	A.D	5A	0.0	00	0.0	1C	0.0	00	0.0	FD	0.0	34
0060	3E	1E	53	10	0.0	0A	20	20	20	20	20	20	0.0	00	00	FC
0070	0.0			20												E2
0080	02	03	26	F1	4E	10	1F	04	13	05	14	03	02	12	20	21
0090	22	15	01	26	15	07	50	09	57	07	67	03	OC.	00	10	0.0
0070	200	2D	E3	05	03	01	01	1D	80	18	71	1C	16	20	50	20
00B0	25	00	A0	5A	00	00	0.0	9E	01	1D	0.0	72	51	DO	1E	20
00C0	6E	28						00								38
00D0	2D	4.0		2C												BC
00E0	52			20												00
OOFO	00	0.0	0.0	00	00	0.0	0.0	00	0.0	00	0.0	00	0.0	00	00	99

HDMI 2 (Check sum:E2, 89)

Addr	00	01	02	03	04	05	06	07	08	09	0A	0B	0C	0D	0E	0F
0000	00	FF	FF	FF	FF	FF	FF	00	1E	6D	01	00	01	01	01	01
0010	01	15	01	03	80	10	09	78	OA.	EE	91	A3	54	4C	99	26
0020	0F	50	54	A1	0.0	0.0	71		81	80	01	01	01	01	01	01
0030	01	01	01	01	01	01					71	38	2D	40	5.0	2C
0040	45	00	ΑO	5A	0.0	00	0.0	1E	1B	21	50	ΑO	51	0.0	1E	30
0050	48	88	35	0.0						1C	0.0	00	0.0	FD	0.0	37
0060	3E	1E	53	10	0.0	0A			20	20	20	20	0.0	0.0	0.0	FC
0070	0.0	4C	47	20						20	20	20	20	20	01	E2
0080	02	03		F1		10		04		05	14	03	02	12	20	21
0090	22	15	01	26			50			07				0.0	20	0.0
0070	200	2D	E3	05		01					71	1C		20	50	20
00B0	25	00	A0	5A				9E	01	1D		72	51	DO	1E	20
00C0	6E	28	55	0.0					0.0	1E	02	3₩	80	18	71	38
00D0	2D	4.0	58	2C	45					0.0	0.0	1E	01	1D	0.0	BC
00E0	52	D0	1E	20		28				5A	0.0	0.0	0.0	1E	00	0.0
OOFO	0.0	0.0	0.0	0.0	0.0	00	0.0	00	0.0	00	0.0	00	0.0	0.0	00	8.9

#### HDMI 3 (Check sum:E2, 79)

Addr	00	01	02	03	04	05	06	07	08	09	0A	0B	0C	0D	0E	0F
0000	00	FF	FF	FF	FF	FF	FF	00	1E	6D	01	00	01	01	01	01
0010	01	15	01	03	80	10	0.9	78	0A	EE	91	A3	54	4C	99	26
0020	0F	50	54	A1	0.0	00	71	4F	81	80	01	01	01	01	01	01
0030	01	01	01	01	01	01	0.2	ЭA	80	18	71	38	2D	40	58	2C
0040	45	0.0	A0	5A	0.0	00	0.0	1E	1B	21	50	AO	51	00	1E	30
0050	48	88	35	0.0	A0	5A	0.0	00	0.0	1C	0.0	0.0	0.0	FD	0.0	3.9
0060	3E	1E	53	10	0.0	OA		20	20	20	20	20	0.0	0.0	0.0	FC
0070	0.0	4C	47	20	54	56	OA.	20	20	20	20	20	20	20	01	E2
0000	02	03	26	F1	4E	10	1F	04	13	05	14	03	02	12	20	21
0090	22	15	01	26	15	07	5.0	09	57	07	67	03	OC.	00	30	00
0070	200	2D	E3	05	03	01	01	1D	80	18	71	1C	16	20	50	2C
00B0	25	00				00	0.0	9E	01	1D	0.0	72	51	DO	1E	20
00C0		28		00		5A	0.0	00	0.0	1E	02	3₩	80	18	71	38
00D0	2D	4.0		2C	45	00	A0	5A	0.0	0.0	0.0	1E	01	1D	0.0	BC
00E0	52	D0	1E	20	BB	28	55	40	A0	5A	0.0	0.0	0.0	1E	00	00
00F0	0.0	0.0	0.0	00	0.0	00	0.0	00	00	00	0.0	00	0.0	0.0	00	79

## 3) HD RGB EDID data(Check sum:CD)

Addr	00	01	02	03	04	05	06	07	08	09	0.4	0B	0C	0D	0E	0F
0000	00	FF		FF	FF	FF			1E		01	00	01	01	01	01
0010	01	15	01	03		10	09	78				¥3	54	4C	99	26
0020	0F	50	54	A1	08	00	81				45	40	31	40	01	01
0030	01	01	01	01	01	01	18	21	5.0			00	1E	30	4.0	0.0
0040	35	00	A0	SA.	0.0	00	00	1C	01	1D	0.0	72	51	DO	1E	20
0050	6E	20	55	0.0	A.O	5A	0.0	00	0.0	1E	0.0	0.0	0.0	FD	0.0	37
0060	3E	1F	46	10	00	OA	20	20	20	20	20	20	00	00	00	FC
0070	00	4C	47	20	54	56	OA.	20	20	20	20	20	20	20	00	CD
0080	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF
0090	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF
0040	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF
0000	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF
00C0	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF
00D0	FF	FF	FF	FF	FF		FF	FF	FF	FF	FF	FF	FF	FF	FF	FF
OOEO	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF	FF
00F0	FF	FF	FF	FF	FF				FF	FF	FF	FF	FF	FF	FF	FF

## 4) HD HDMI EDID data HDMI 1 (Check sum:B4, 65)

$^{\rm Addr}$	00	01	02	03	04	0.5	06	07	08	09	0.4	OB	0C	0D	0Œ	OF
0000	00	FF	FF	FF	FF	FF	FF	00	1E	6D	01	00	01	01	01	01
0010	01	15	01	03	80	10	09	78	OΑ	EE	91	73	54	4C	99	26
0020	0F	50	54	A1	08	00	81	C0	61	4.0	45	4.0	31	40	01	01
0030	01	01	01	01	01	01	1B	21	50	A0	51	00	1E	30	48	88
0040	35	00	WO.	5A	00	0.0	00	10	01	1D	00	72	51	DO	1E	20
0050	6E	28	55	00	Y0	5A	00	00	00	1E	00	00	00	FD	00	3¥
0060	3E	1F	46	10	00	A0	20	20	20	20	20	20	00	00	00	FC
0070	00	4C	47	20	54	56	0A	20	20	20	20	20	20	20	01	B4
0080	02	03	20	F1	4E	10	1F	84	13	05	14	03	02	12	20	21
0090	22	15	01	26	15	07	50	09	57	07	65	03	0C	00	10	00
0040	01	1D	80	18	71	10	16	20	58	2C	25	00	A0	SA	00	00
00B0	00	9E	01	1D 1E	00	0 A	51	DO BA	OC.	20 E0	40 2D	80	35	00	A0	5A 00
0000	40	54	00	00	8C	18	D0 02	3A	20 80	18	71	38	2D	3E	96 58	2C
OOEO	45	00	40	5 A	00	00	00	1E	01	1D	80	D0	72	10		20
																65
00F0			25		70		00	00			00					

HDMI 2 (Check sum:B4, 55)

Addr	00	01	02	03	04	05	06	07	08	09	07	0B	0C	0D	0E	0F
0000	00	FF	FF	FF	FF	FF	FF	00	1E	6D	01	00	01	01	01	01
0010	01	15	01	0.3	80	10	09	78	0.4	EE	91	<b>A3</b>	54	4C	99	26
0020	0F	50	54	A1	08	0.0	81	C0	61	40	45	40	31	40	01	01
0030	01	01	01	01	01	01	18	21	50	A0	51	0.0	1E	30	48	88
0040	35	00	W0	5A	00	00	00	10	01	1D	00	72	51	D0	1E	20
0050	6E	28	55	0.0	70	SA.	00	0.0	00	1E	00	0.0	00	FD	0.0	3A
0060	3E	1F	46	10	00	A.O	20	20	20	20	20	20	00	0.0	0.0	FC
0070	00	4C	47	20	54	56	0A	20	20	20	20	20	20	20	01	B4
0080	02	0.3	20	F1	4E	10	1F	84	13	05	14	03	02	12	20	21
0090	22	15	01	26	15	07	50	09	57	07	65	03	0C	0.0	20	00
0040	01	1D	80	18	71	10	16	20	58	2C	25	0.0	A0	5A	0.0	0.0
00B0	00	9E	01	1D	00	80	51	D0	0C	20	40	80	35	00	A0	5A
00C0	00	00	00	1E	8C	A.0	DO	0A	20	EO	2D	10	10	ЗE	96	00
OODO	70	SA	00	0.0	00	10	02	3A	80	10	71	38	2D	40	50	2C
00E0	45	00	70	5.4	00	00	00	1E	01	1D	80	DO	72	1C	16	20
00F0	10	2C	25	8:0	W0	5A	00	0.0	00	9E	00	00	00	00	00	55

HDMI 3 (Check sum:B4, 45)

Addr	00	01	02	03	04	05	06	07	08	09	0.4	0B	0C	0D	0E	0F
0000	00	FF	FF	FF	FF	FF	FF	00	1E	6D	01	00	01	01	01	01
0010	01	15	01	03	80	10	09	78	0A	EE	91	A3	54	4C	99	26
0020	0F	50	54	A1	08	0.0	81	C0	61	4.0	45	40	31	40	01	01
0030	01	01	01	01	01	01	1B	21	50	A.O	51	0.0	1E	30	40	0.0
0040	35	00	70	54	00	00	00	10	01	1D	00	72	51	DO	1E	20
0050	6E	28	55	00	90	5A	00	0.0	00	1E	00	0.0	00	FD	00	3 A
0060	3E	1F	46	10	00	A.O	20	20	20	20	20	20	00	0.0	00	FC
0070	0.0	4C	4.7	20	54	56	OA	20	20	20	20	20	20	20	01	B4
0080	02	03	20	F1	4E	10	1F	04	13	05	14	03	02	12	20	21
0090	22	15	01	26	15	07	50	09	57	07	65	03	0C	0.0	30	00
0070	01	1D	80	10	71	10	16	20	50	2C	25	0.0	A0	SA.	00	00
00B0	0.0	9E	01	1D	00	80	51	DO	0C	20	40	80	35	0.0	A0	SA
00C0	0.0	00	0.0	1E	8C	OA	DO	84	20	E0	2D	10	10	3E	96	00
OODO	A0	5A	00	00	00	18	02	34	80	18	71	38	2D	40	58	2C
00E0	45	00	¥0	54	00	00	00	1E	01	1D	80	DO	72	1C	16	20
00F0	10	2C	25	80	70	SA	00	00	00	9E	00	0.0	0.0	0.0	00	45

## - Model List

HD(CCFL)	FHD(CCFL)	HD(LED)	FHD Small(LED)	FHD(LED)
32LK330-SB	32LK450-SA	19LV2500-SA		32LV4500-SA
	37LK450-SA	22LV2500-SA		42LV4500-SA
	42LK450-SA	26LV2500-SA		32LV3500-SA
	47LK450-SA	32LV2500-SA		37LV3500-SA
	32LK430-SA			42LV3500-SA
				47LV3500-SA
				55LV3500-SA

## 4.5. Outgoing condition Configuration

 When pressing IN-STOP key by SVC remocon, Red LED are blinked alternatively. And then Automatically turn off. (Must not AC power OFF during blinking)

## 4.6. Hi-pot Test

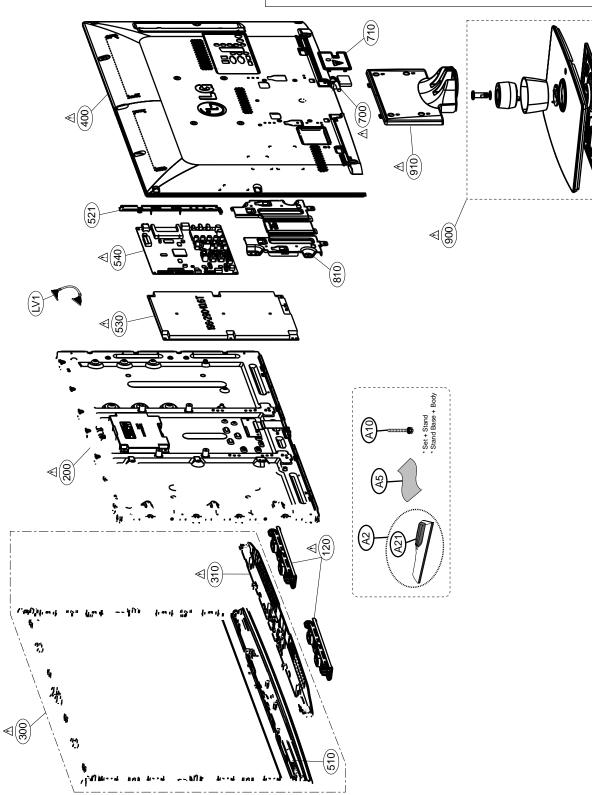
Confirm whether is normal or not when between power board's ac block and GND is impacted on 1.5 kV/min at 100mA(GND) and 3kV/min at 100mA(Signal)

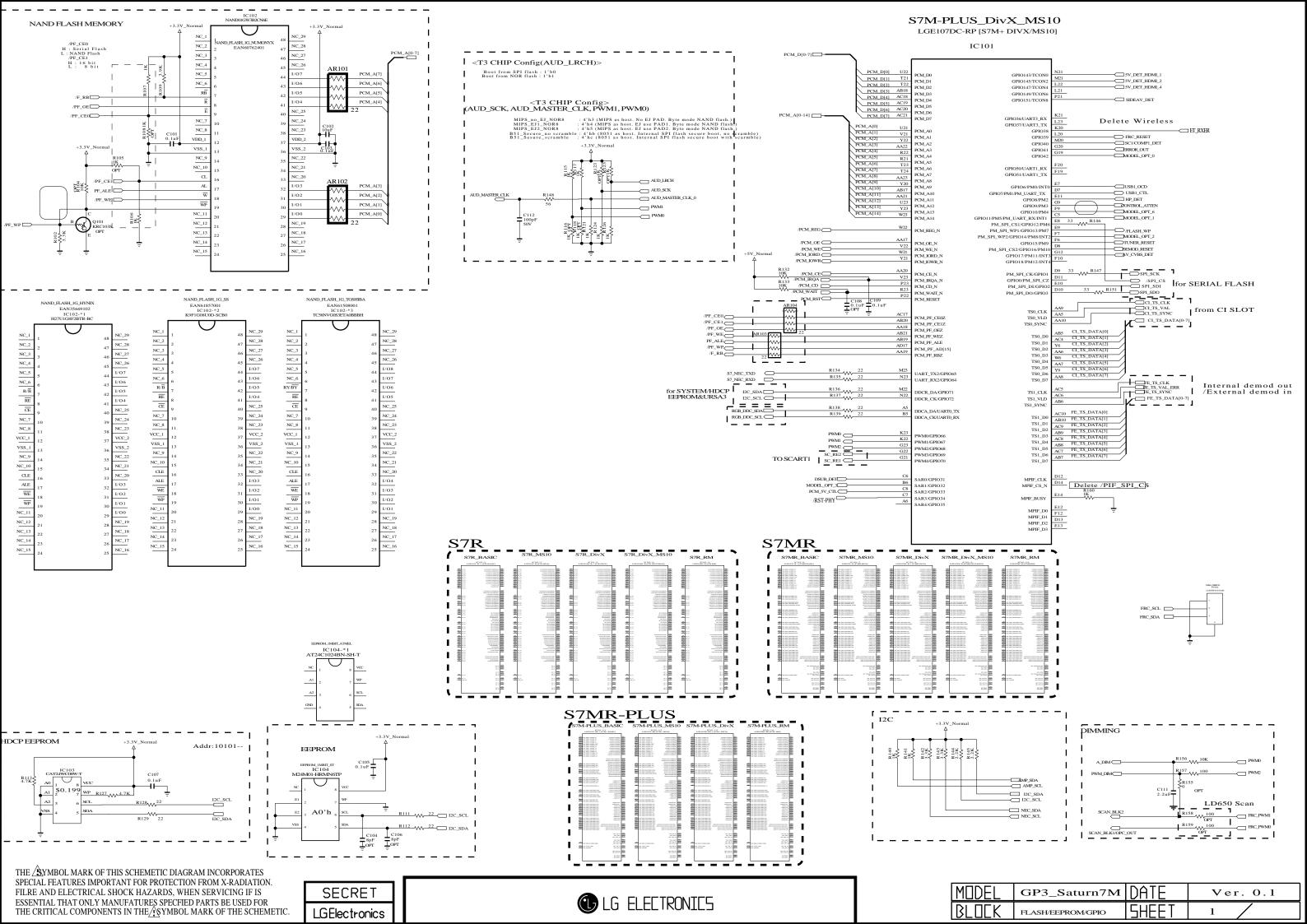
## **EXPLODED VIEW**

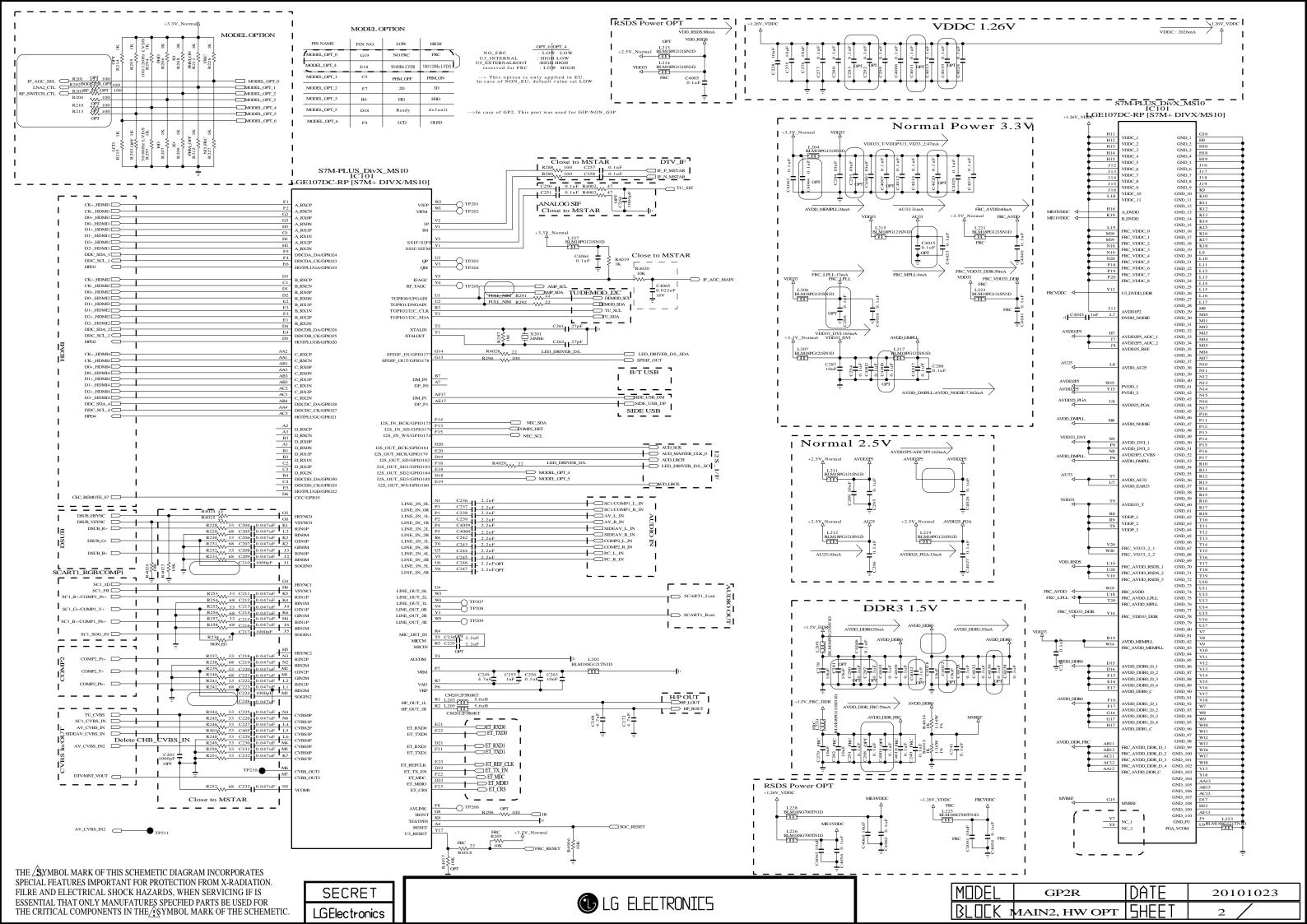
#### IMPORTANT SAFETY NOTICE

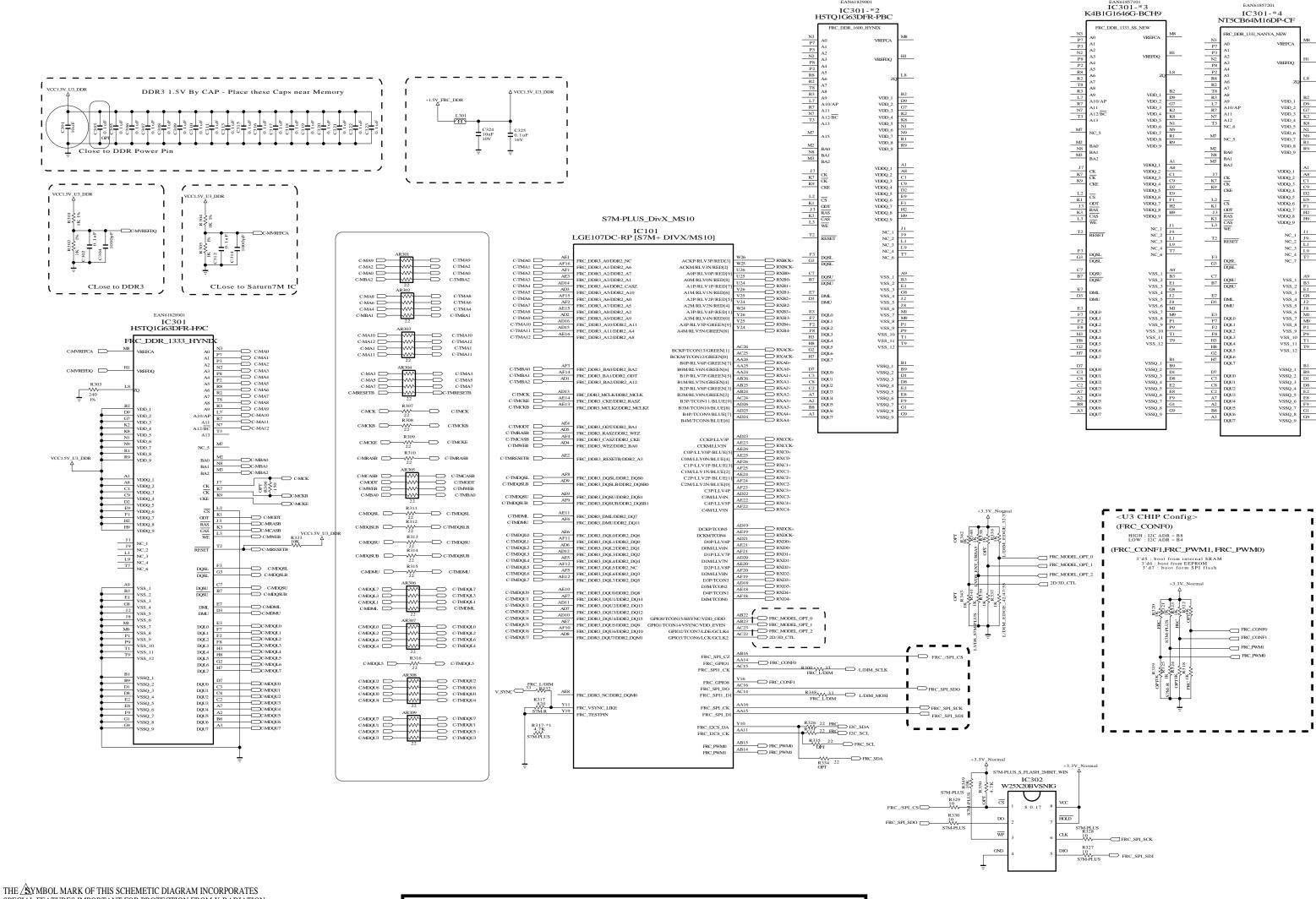
Many electrical and mechanical parts in this chassis have special safety-related characteristics. These parts are identified by  $\triangle$  in the Schematic Diagram and EXPLODED VIEW. It is essential that these special safety parts should be replaced with the same components as recommended in this manual to prevent X-RADIATION, Shock, Fire, or other Hazards.

Do not modify the original design without permission of manufacturer.





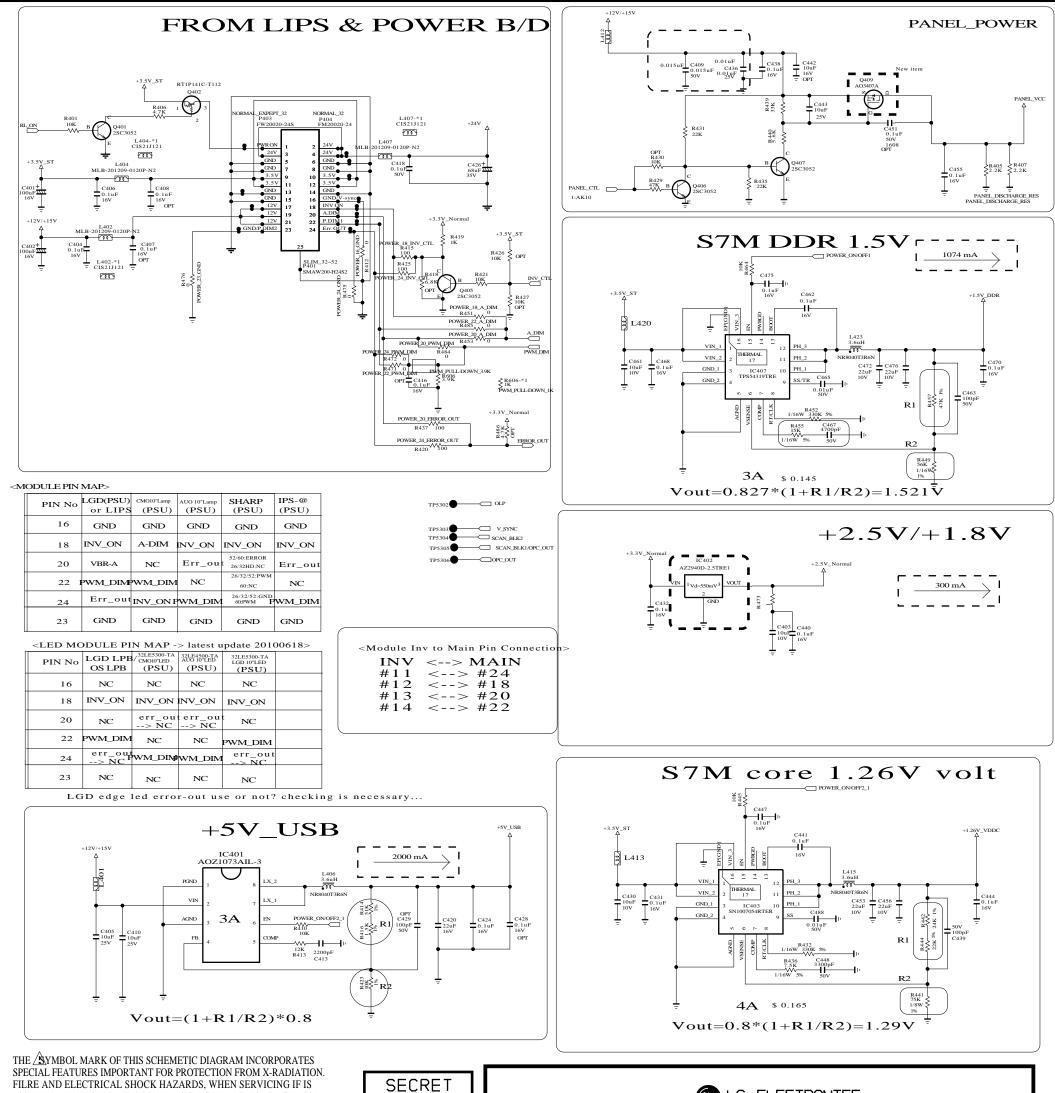




SECRET LGElectronics



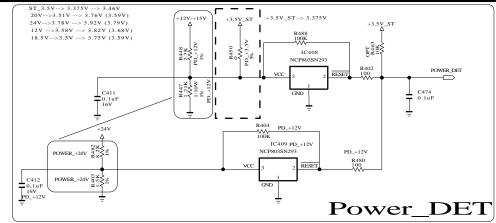
MODEL	GP2R	DATE	20101023
BLOCK	FRC_DDR	SHEET	3 /

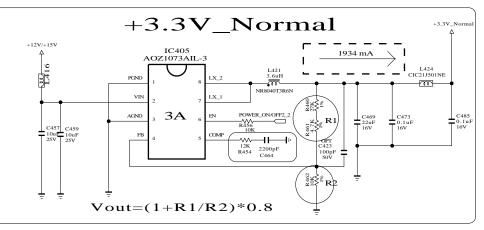


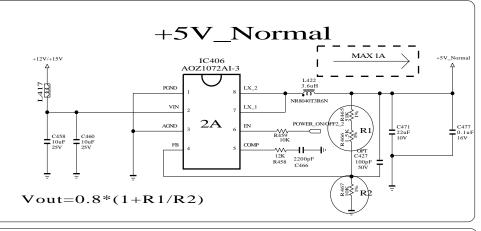
**LGElectronics** 

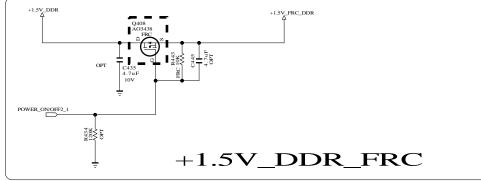
ESSENTIAL THAT ONLY MANUFATURES SPECFIED PARTS BE USED FOR

THE CRITICAL COMPONENTS IN THE !\SYMBOL MARK OF THE SCHEMETIC.



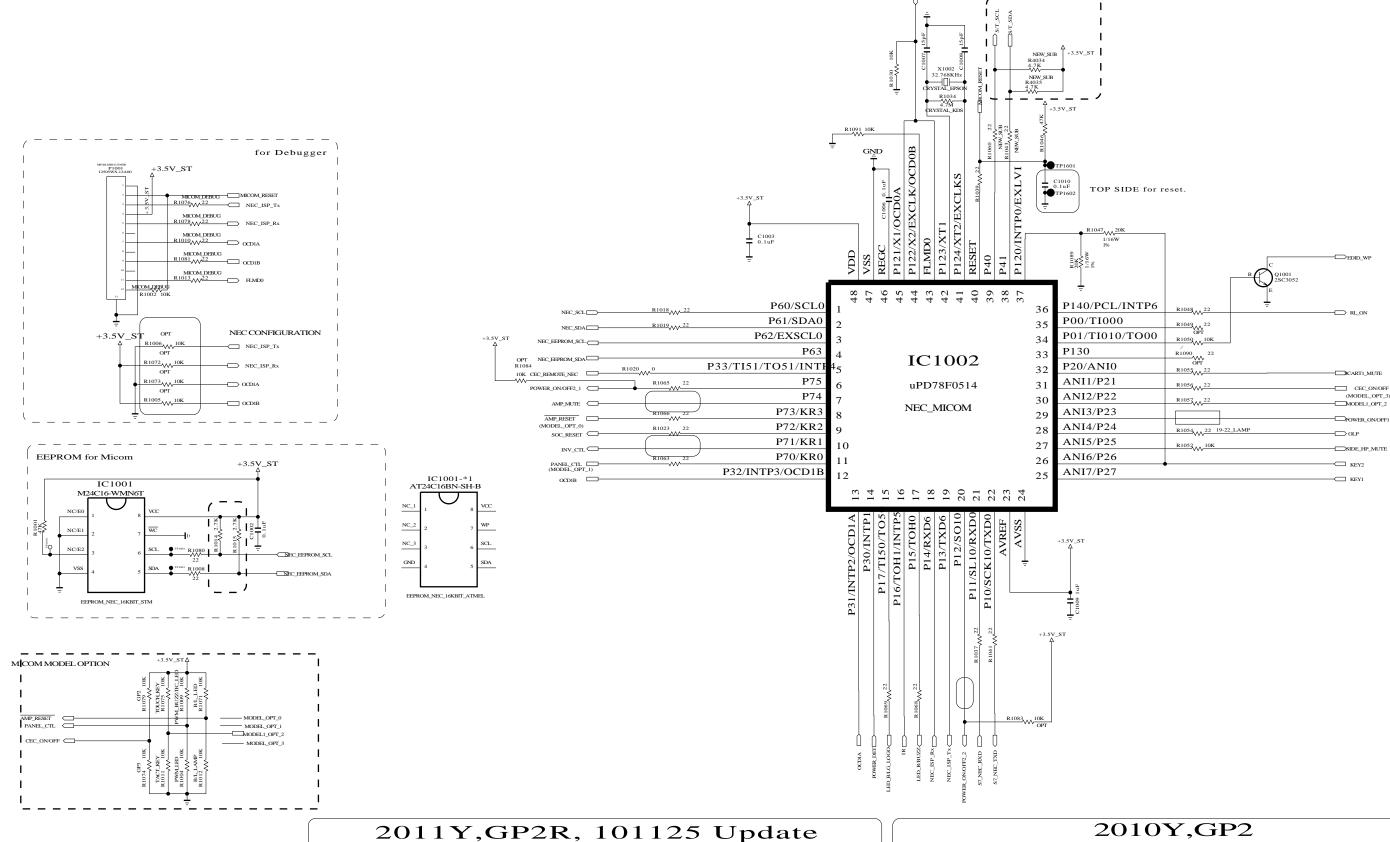






LG ELECTRONICS

DATE 20101023 GP2R BLOCK POWER\_LARGE SHEET 4



	MODEL	OPTION	
PIN NAME	PIN NO.	HIGH	LOW
MODEL_OPT_0	8	B/L_LED	B/L_LAMP
MODEL_OPT_1	11	PWM_BUZZ/IIC_LED	PWM_LED
MODEL_OPT_2	30	TOUCH_KEY	TACT_KEY
MODEL_OPT_3	31	GP2	GP3
		IIC for LED Breathin nal for LED Lightin	

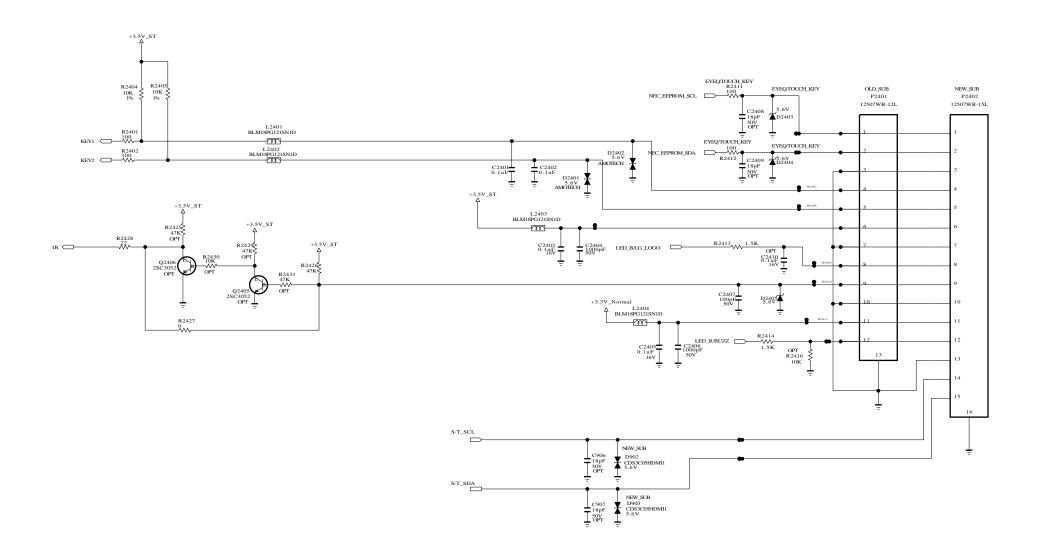
	MODEL_OPT_0	MODEL_OPT_1	MODEL_OPT_2	MODEL_OPT_3	Description
-	LOW	LOW	LOW	LOW	LK330/LK430 for KR/US 10Y EYE-Q Sensor KEY & PWM LED & No Buzz & No LED Blink
	LOW	LOW	LOW	HIGH	LK330/LK430/LK530 KEY & PWM LED & No Buzz & No LED Blink
	LOW : LED HIGH : LAMP	HIGH	HIGH	LOW	LV25/LV35/LV45/LW45/LV55/LK45/LK55 S/T & IIC LED & NO BUZZ & LED Blink
		HIGH	LOW	LOW	TBD IIC LED(09Y IIC Protocol) & No BUZZ
		Low	HIGH	LOW	TBD S/T & IIC LED & No Buzz & LED Blink

2010Y,GP2								
MODEL	OPTION	I	MODEL_OPT_0	MODEL_OPT_1	MODEL_OPT_2	MODEL_OPT_3		
PIN NO.	HIGH	LOW	LOW	LOW	LOW	LOW	LD350/450/550 PWM LED & No Buzz & No LED Blink	
-			HIGH	LOW	HIGH	LOW	19/22/26LE5300/5300 IIC LED & PWM IIC BUZZ	
			HIGH	HIGH	HIGH	LOW	32/37/42/47/55LE5300 IIC LED & PWM BUZZ	
			LOW	HIGH	LOW	LOW	LD420 IIC LED(09Y IIC Protocol) & No BU	
			HIGH	LOW	LOW	HIGH	LE7300 GPIO LED & NO BUZZ	
) : For model	that use LED Lighti	ng used PWM Signal						
	PIN NO.  8  11  30  31  ZZ/HC_LED:	B/L_LED     PWM_BUZZ/IC_LED     TOUCH_KEY     I GPIO_LED     ZZ/IIC_LED: For model that use	MODEL OPTION  PIN NO. HIGH LOW -  8 B/L_LED B/L_LAMP -  11 PWM_BUZZ/IC_LED PWM_LED -  30 TOUGH_KEY TACT_KEY -  31 GPIO_LED NON_GPIO_LED -	MODEL OPTION	MODEL OPTION	MODEL_OPTION	MODEL OPTION         MODEL_OPT_0         MODEL_OPT_1         MODEL_OPT_2         MODEL_OPT_3           PIN NO.         HIGH         LOW         LOW         LOW         LOW         LOW         LOW         LOW         LOW         LOW         HIGH         LOW         HIGH         LOW         HIGH         LOW         HIGH         LOW         LOW         LOW         HIGH         LOW         HIGH	





MODEL	GP2R	DATE	20101125
BLOCK	MICOM Rev.4	SHEET	5 /

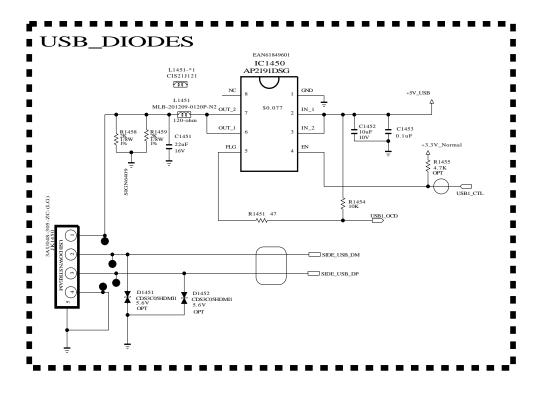


THE /SYMBOL MARK OF THIS SCHEMETIC DIAGRAM INCORPORATES
SPECIAL FEATURES IMPORTANT FOR PROTECTION FROM X-RADIATION.
FILRE AND ELECTRICAL SHOCK HAZARDS, WHEN SERVICING IF IS
ESSENTIAL THAT ONLY MANUFATURES SPECFIED PARTS BE USED FOR
THE CRITICAL COMPONENTS IN THE SYMBOL MARK OF THE SCHEMETIC.





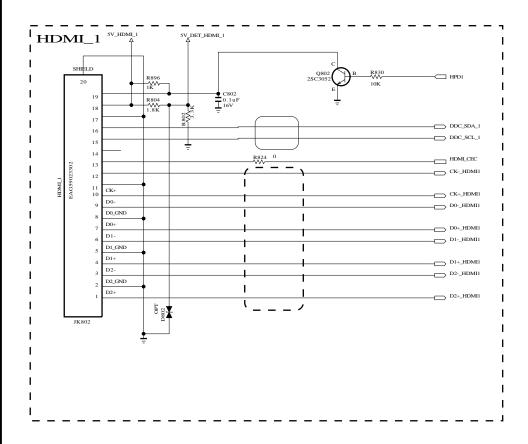
MODEL	GP2R	DATE	20101023
BLOCK	IR/CONTROL-L	SHEET	6 /

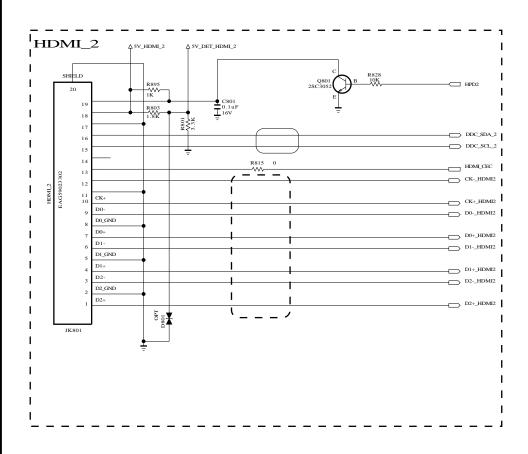


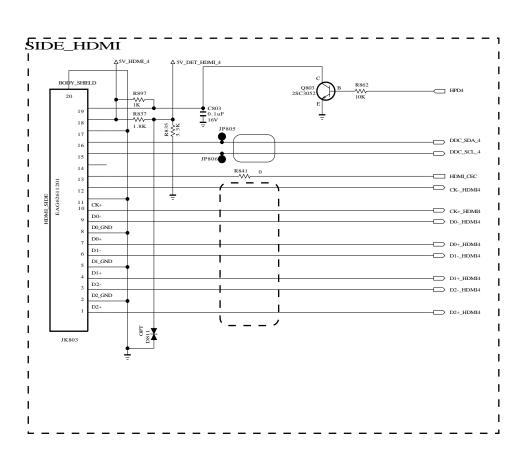




MODEL	GP2R	DATE	2010102	3
BLOCK	USB_OCP_DIG	ODESHEET	7 /	

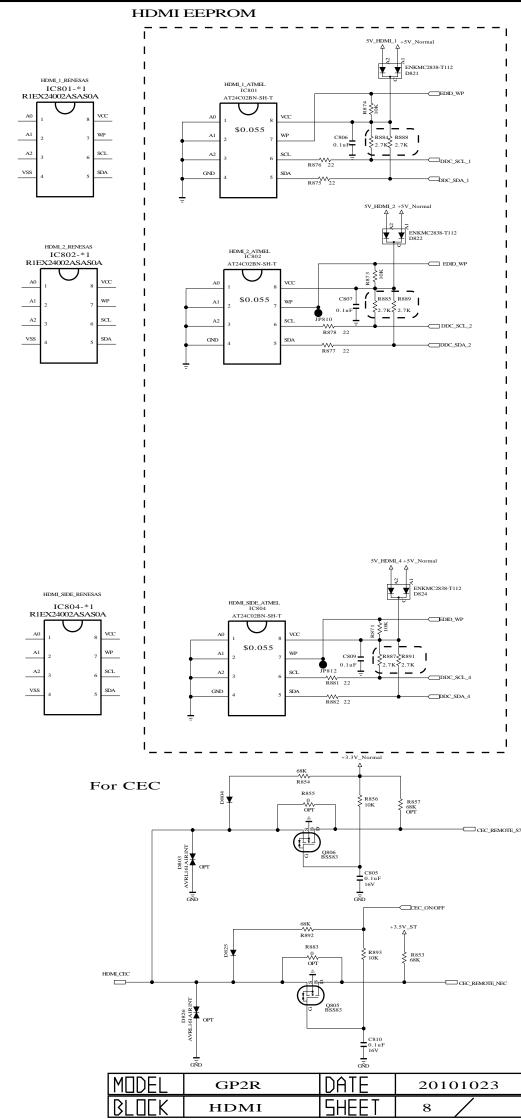




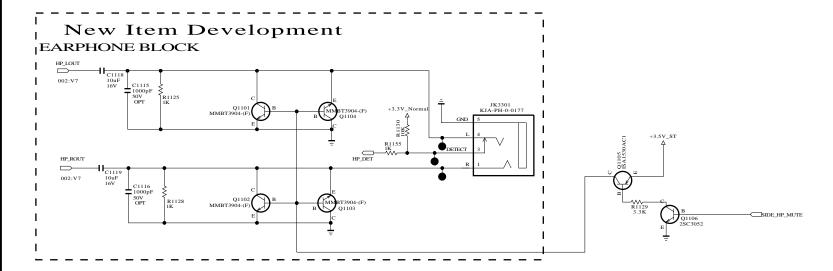


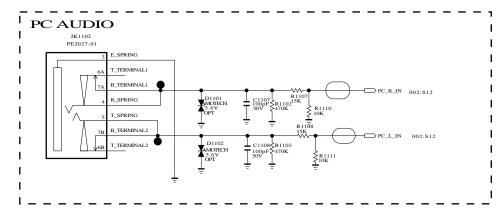
SECRET LGElectronics

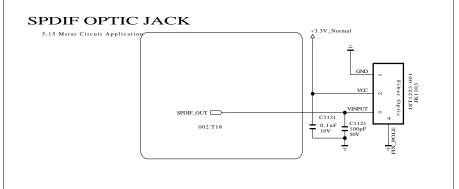


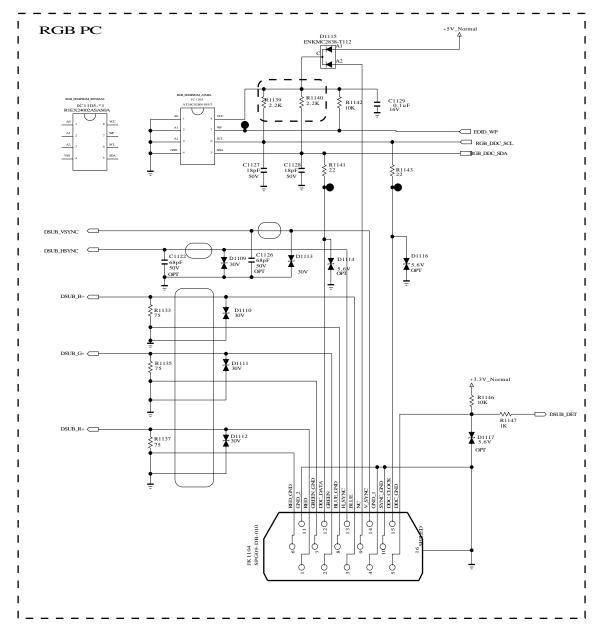


## RGB/SPDIF/PC/HP







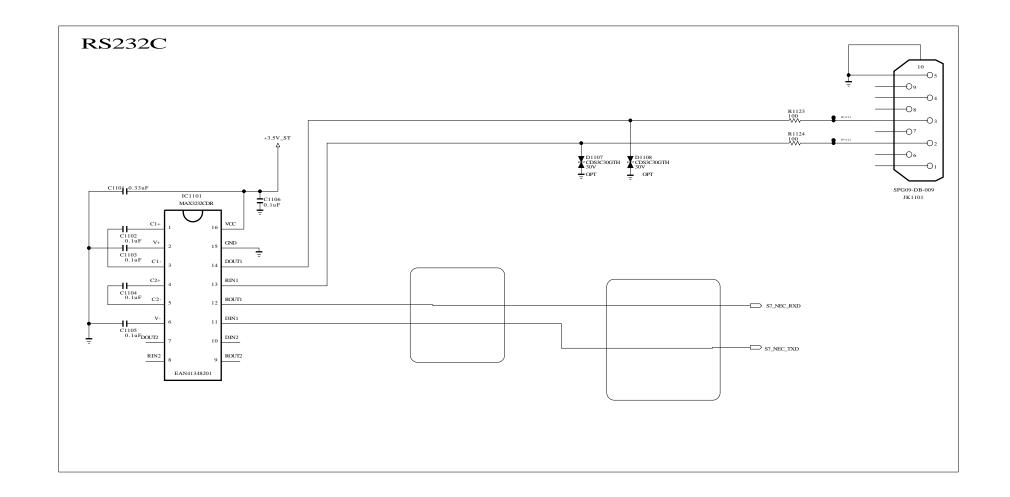


THE SYMBOL MARK OF THIS SCHEMETIC DIAGRAM INCORPORATES SPECIAL FEATURES IMPORTANT FOR PROTECTION FROM X-RADIATION. FILRE AND ELECTRICAL SHOCK HAZARDS, WHEN SERVICING IF IS ESSENTIAL THAT ONLY MANUFATURES SPECFIED PARTS BE USED FOR THE CRITICAL COMPONENTS IN THE SYMBOL MARK OF THE SCHEMETIC.

SECRET LGElectronics



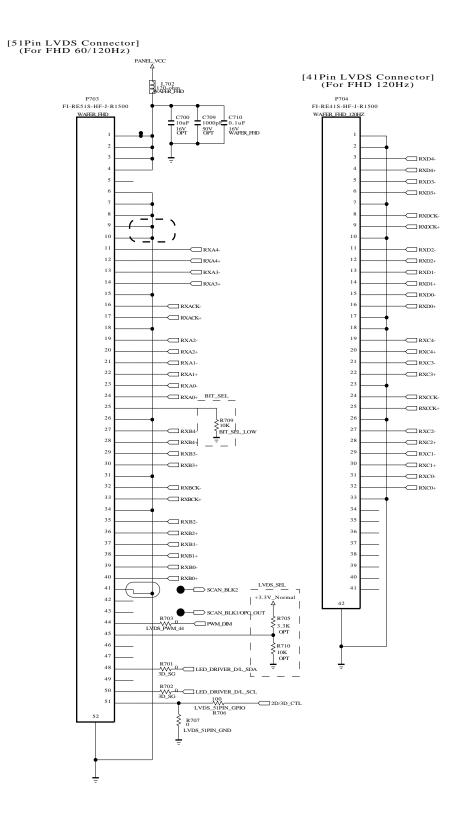
MODEL	GP2R	DATE	20101023
BLOCK	RGB/SPDIF/HP	SHEET	9 /

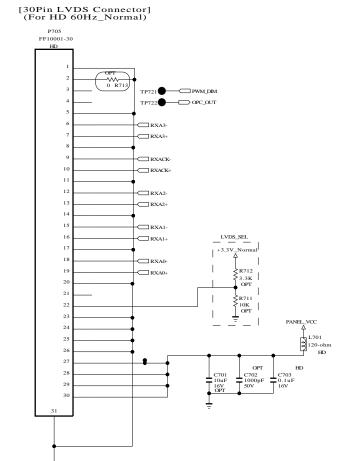






MODEL	GP2R	DATE	20101023
BLOEK	RS232C_9PIN	SHEET	10 /

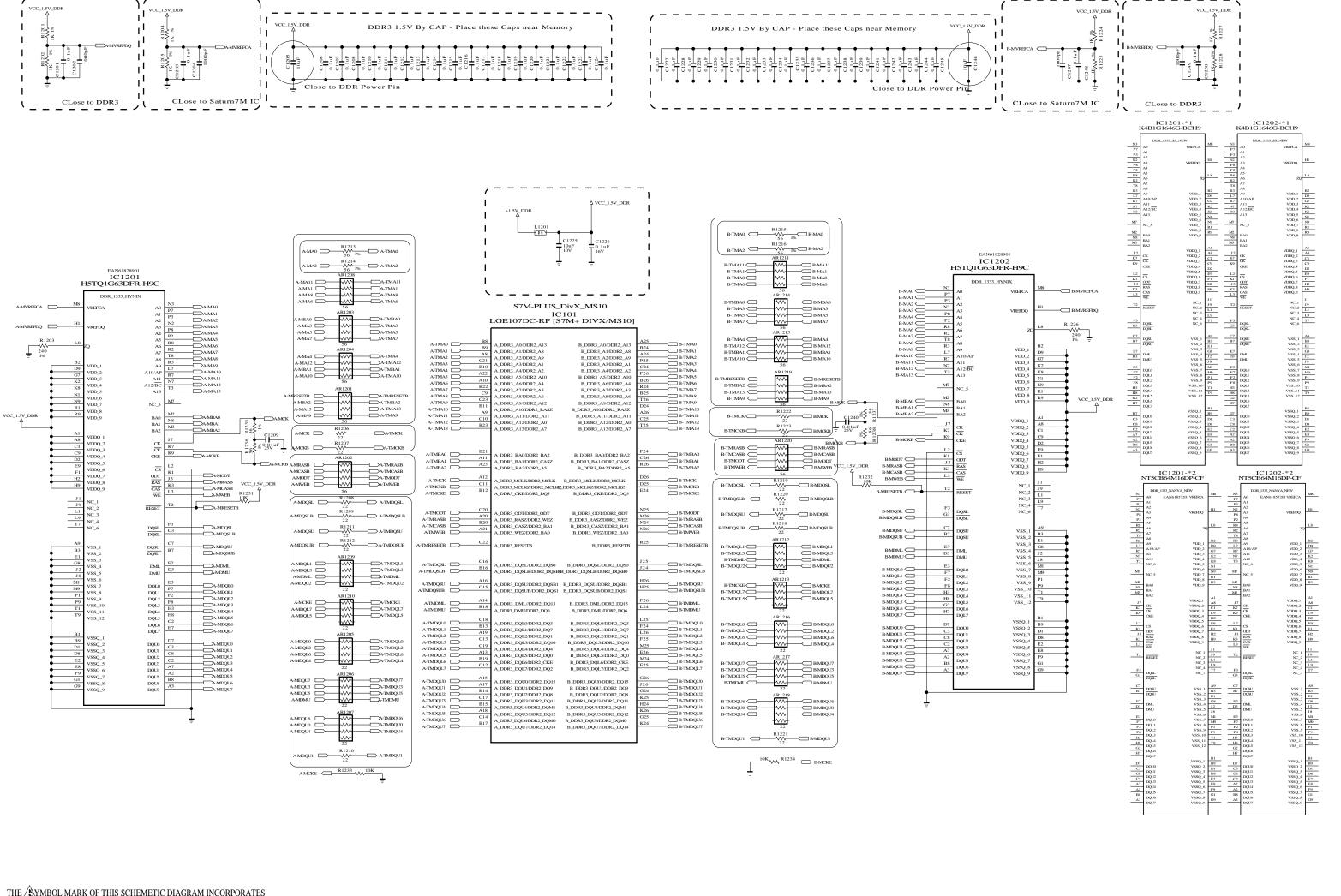








MODEL	GP2R	DATE	20101023
BLOCK	LVDS_LARGE	SHEET	11

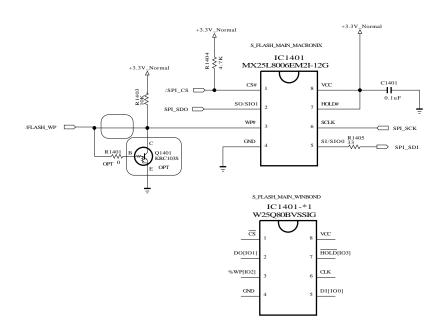


SPECIAL FEATURES IMPORTANT FOR PROTECTION FROM X-RADIATION.
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ESSENTIAL THAT ONLY MANUFATURES SPECFIED PARTS BE USED FOR
THE CRITICAL COMPONENTS IN THE SYMBOL MARK OF THE SCHEMETIC.





ME	IDEL .	GP2R	DATE	20101023
BL	. 🗆 🗆 K	DDR_256	SHEET	12 /

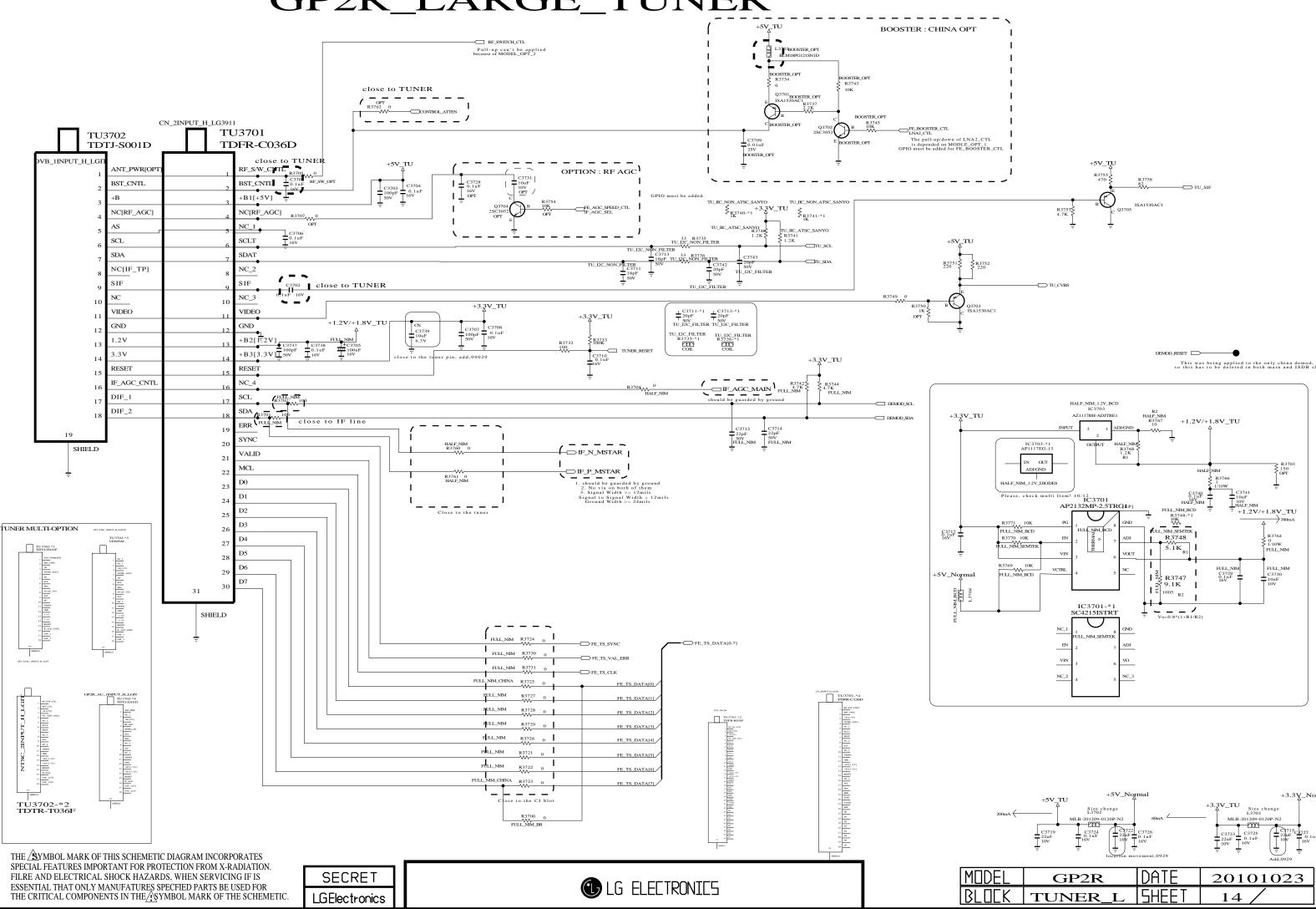




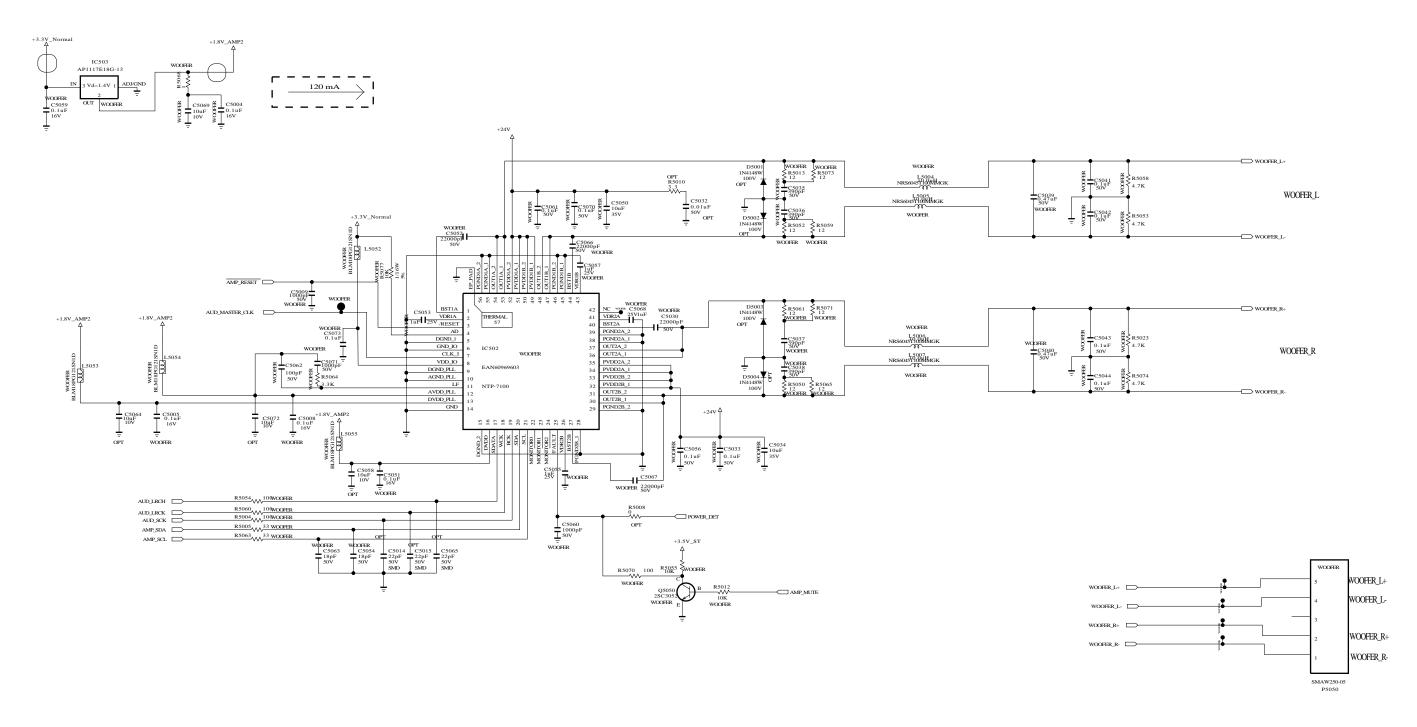


MODEL	GP2R	DATE	20101023
3LOEKs	FLASH 11	MIBSHEET	13/

## GP2R\_LARGE\_TUNER



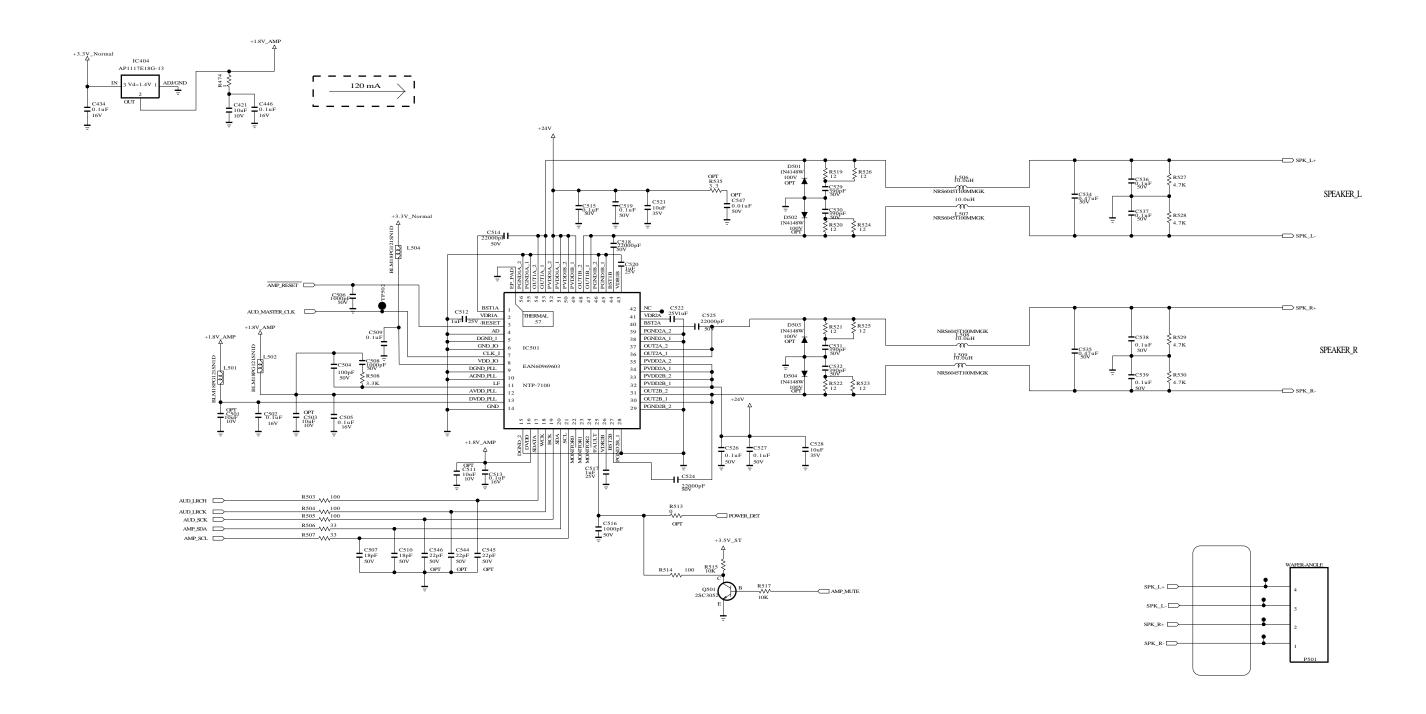
# WOOFER AMP

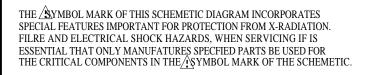






MODEL	GP2R	DATE	20101023
BLOEKW	OOFER N	n <b>e</b> HEET	15/

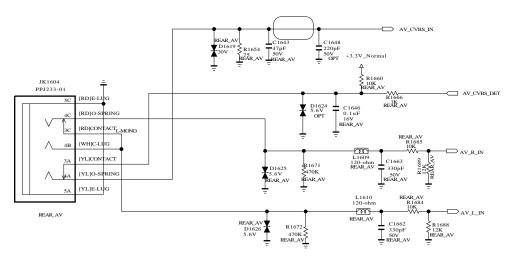


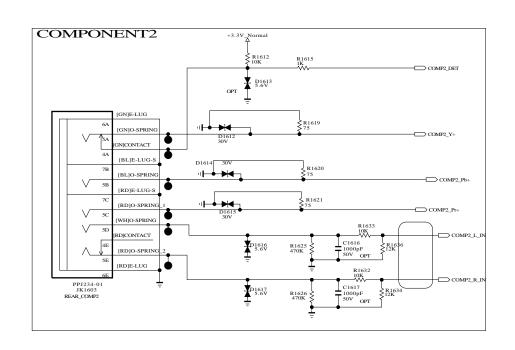


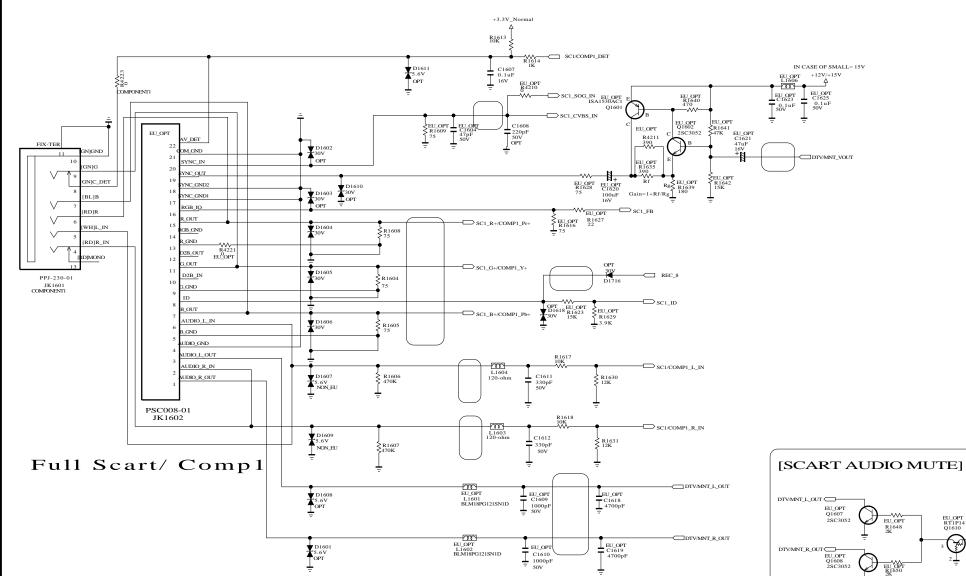


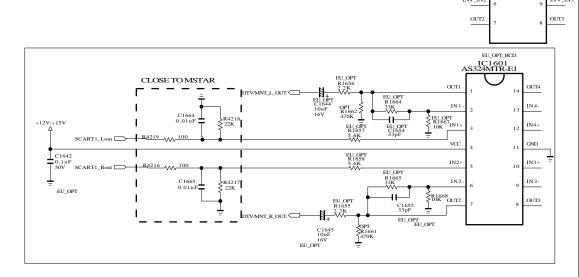


MODEL	GP2R	DATE	20101023
BLOCK	AMP NTP	SHEET	16/









ETHERNET FOR DVB\_T2

ET\_RXDO

ET\_RXDO

ET\_RXDI

ET\_RTADI

ET\_REF\_CLK

ET\_RT\_EN

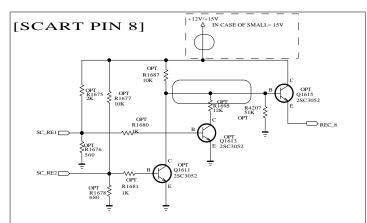
ET\_MCC

ET\_MDIO

ET\_CRS

ET\_RXER

ET\_RXER



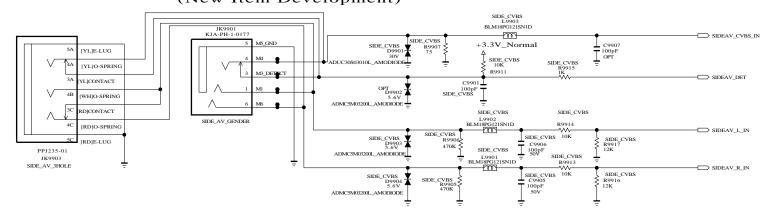
THE AMY BOL MARK OF THIS SCHEMETIC DIAGRAM INCORPORATES SPECIAL FEATURES IMPORTANT FOR PROTECTION FROM X-RADIATION. FILRE AND ELECTRICAL SHOCK HAZARDS, WHEN SERVICING IF IS ESSENTIAL THAT ONLY MANUFATURES SPECFIED PARTS BE USED FOR THE CRITICAL COMPONENTS IN THE SYMBOL MARK OF THE SCHEMETIC

SECRET LGElectronics

LG ELECTRONICS

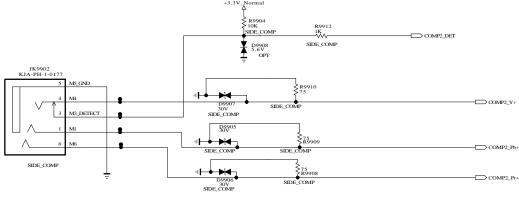
MODEL GP2R DATE 20101023
BLOCKREAR JACK SHEET 17

## SIDE CVBS PHONE JACK (New Item Development)



## SIDE COMPONENT PHONE JACK

(New Item Developmen)

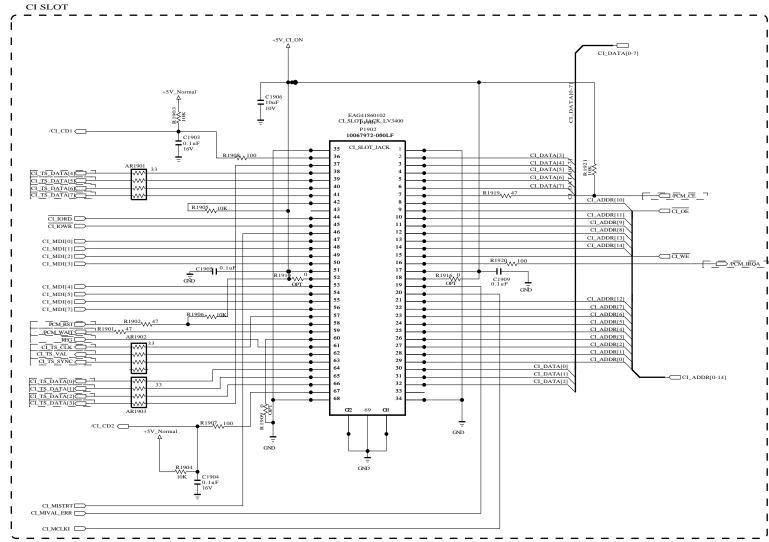


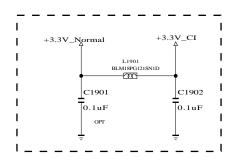
SECRET		
LGElectronics		

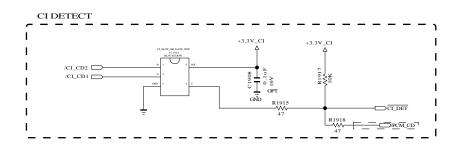


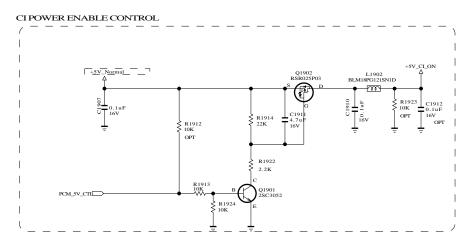
MODEL	GP2R	DATE	20101023
BLOEK :	SIDE_JACK	SHEET	18 /

## CI Region







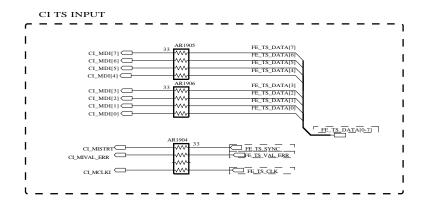


THE SYMBOL MARK OF THIS SCHEMETIC DIAGRAM INCORPORATES SPECIAL FEATURES IMPORTANT FOR PROTECTION FROM X-RADIATION. FILRE AND ELECTRICAL SHOCK HAZARDS, WHEN SERVICING IF IS ESSENTIAL THAT ONLY MANUFATURES SPECFIED PARTS BE USED FOR THE CRITICAL COMPONENTS IN THE SYMBOL MARK OF THE SCHEMETIC.

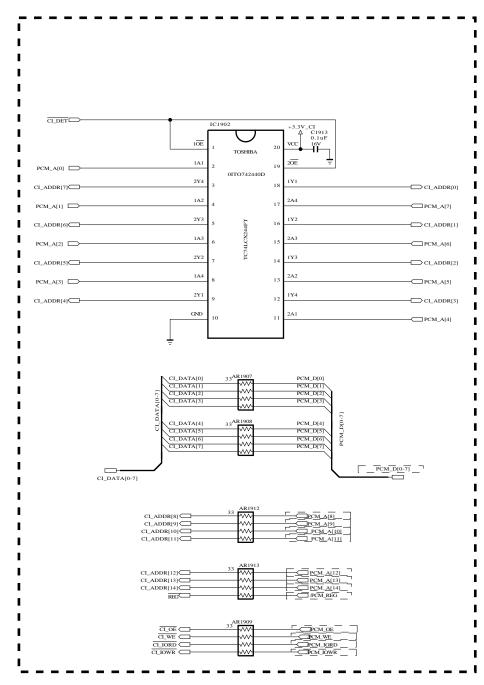




\* Option name of this page : CI\_SLOT (because of Hong Kong)

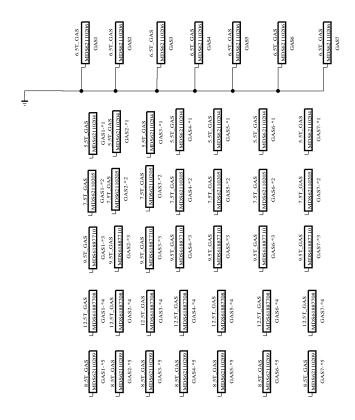


CI HOST I/F



MODEL	GP2R	DATE	20101023
BLOCK	PCMCI	SHEET	20 /

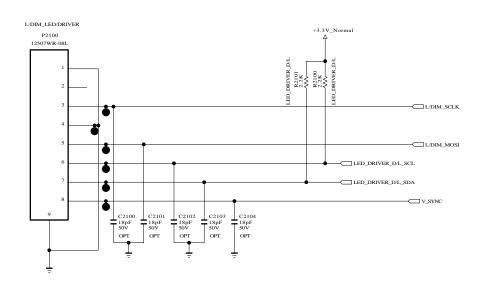
## SMD GASKET







MODEL	GP2R	DATE	20101023
BLOCK	SMD_GAS	SHEET	20 /

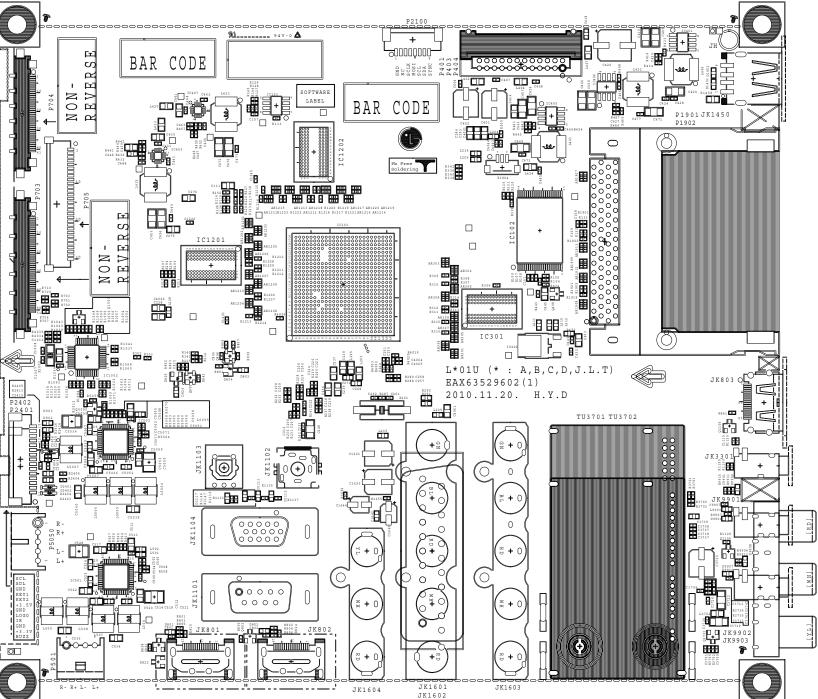


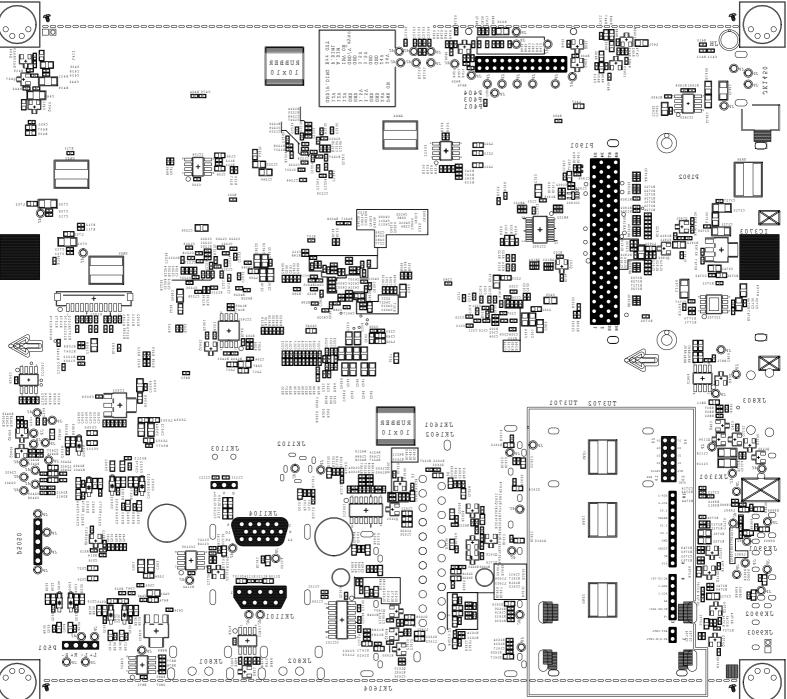
THE SYMBOL MARK OF THIS SCHEMETIC DIAGRAM INCORPORATES SPECIAL FEATURES IMPORTANT FOR PROTECTION FROM X-RADIATION. FILRE AND ELECTRICAL SHOCK HAZARDS, WHEN SERVICING IF IS ESSENTIAL THAT ONLY MANUFATURES SPECFIED PARTS BE USED FOR THE CRITICAL COMPONENTS IN THE SYMBOL MARK OF THE SCHEMETIC.





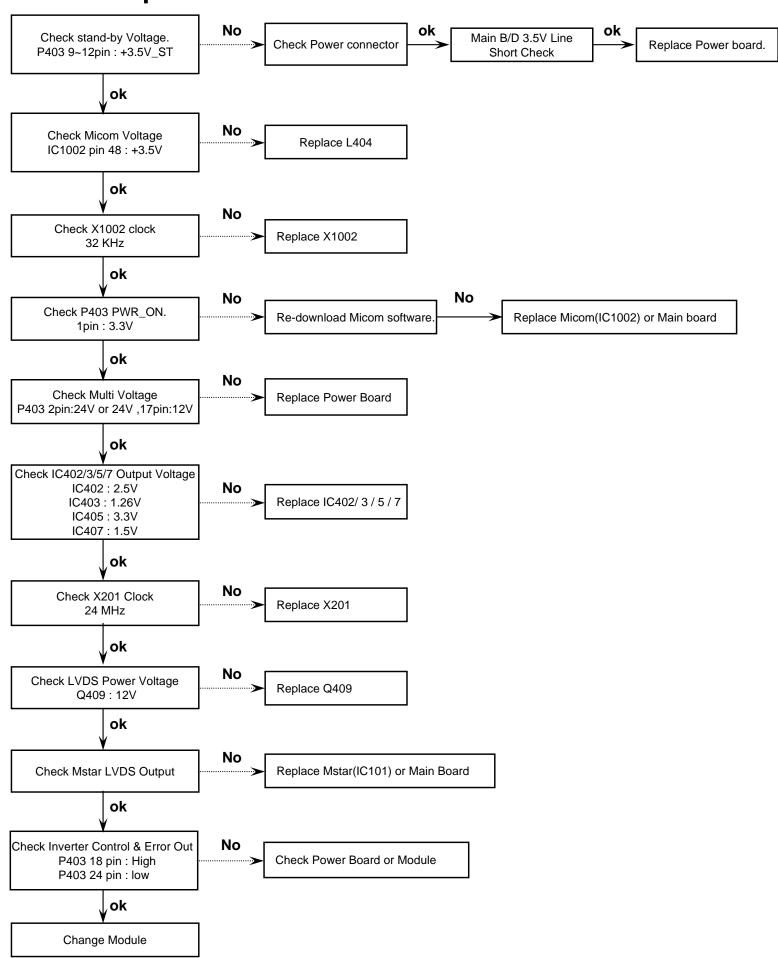
MODEL	GP2R	DATE	20101023
BLOCK	L/DIM_LED	SHEET	21 /



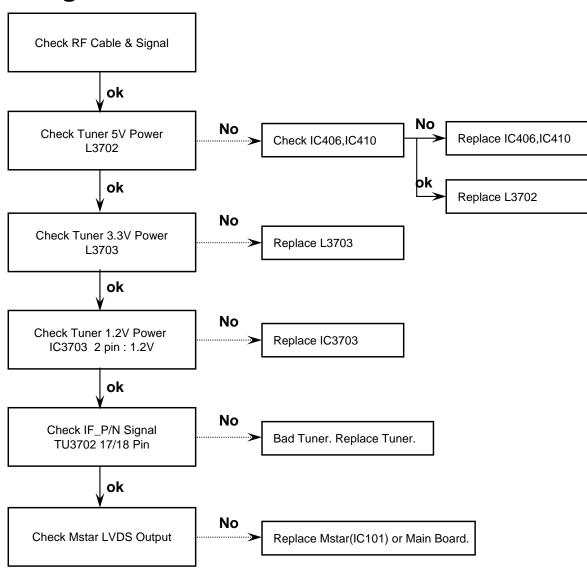




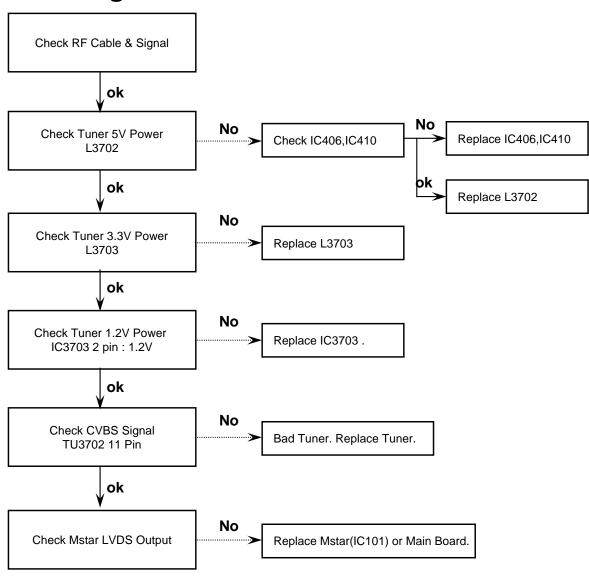
### 1. Power-up boot check



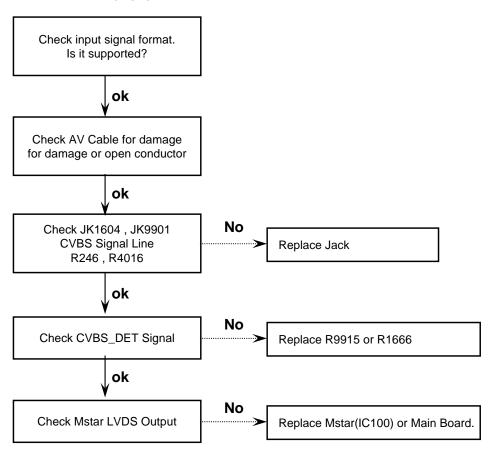
# 2. Digital TV Video



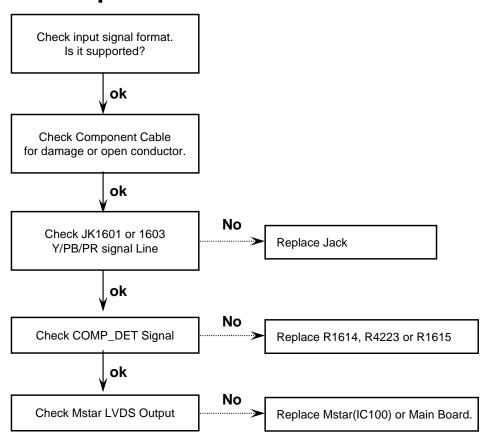
## 3. Analog TV Video



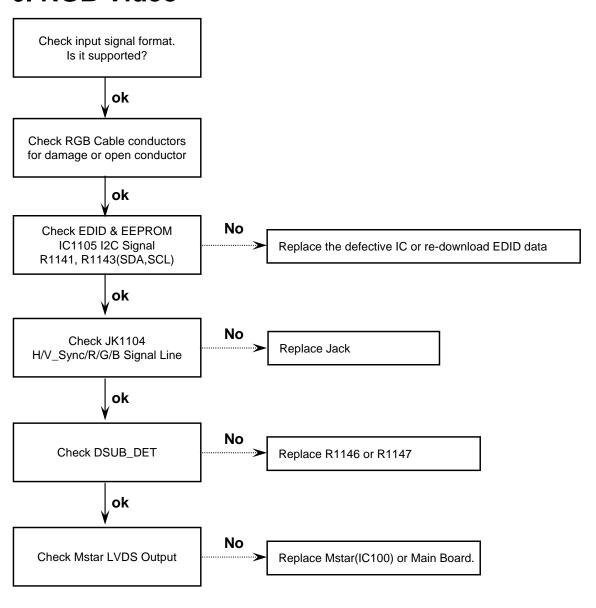
#### 4. AV Video



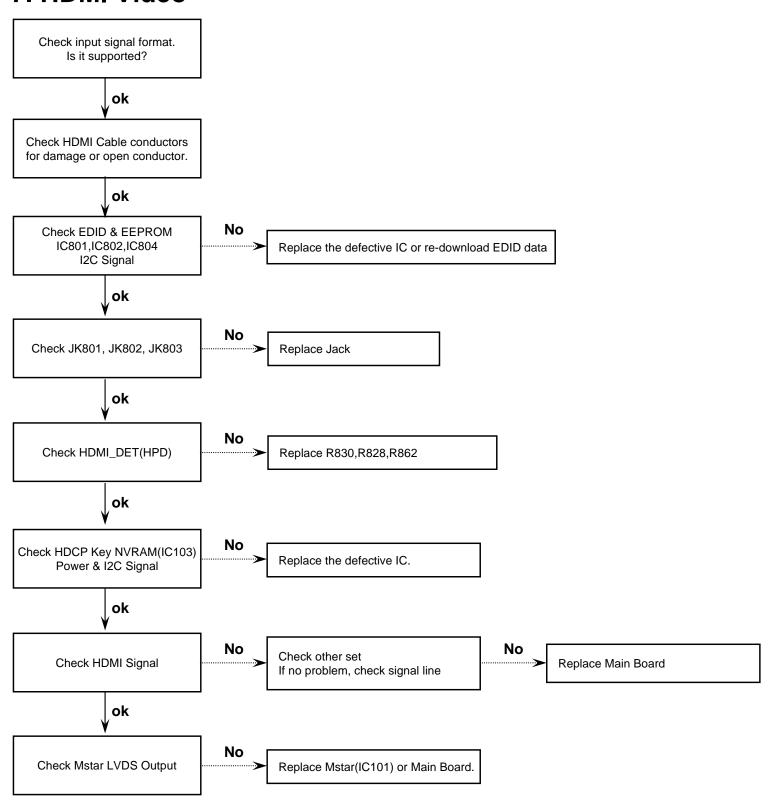
## 5. Component Video



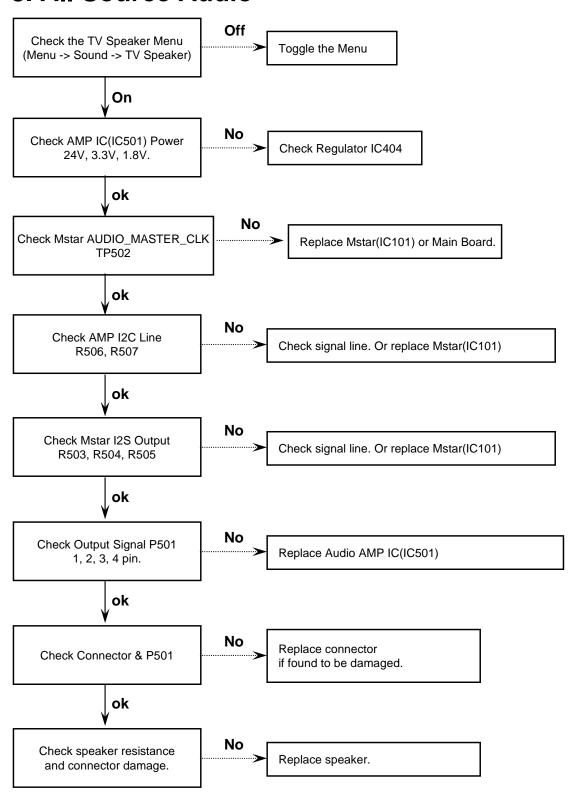
### 6. RGB Video



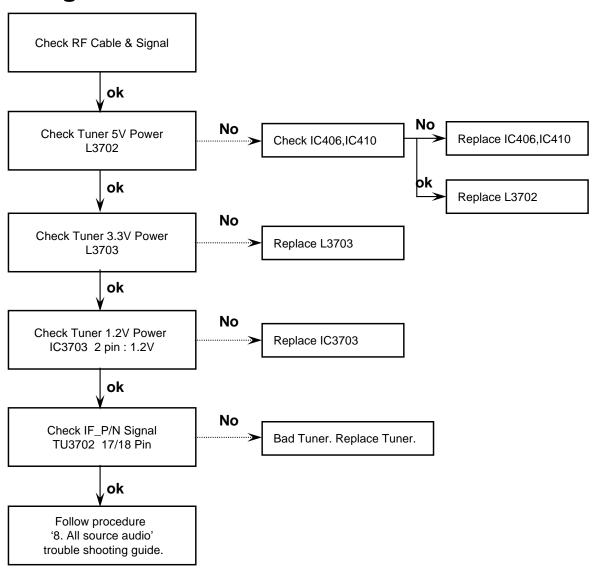
#### 7. HDMI Video



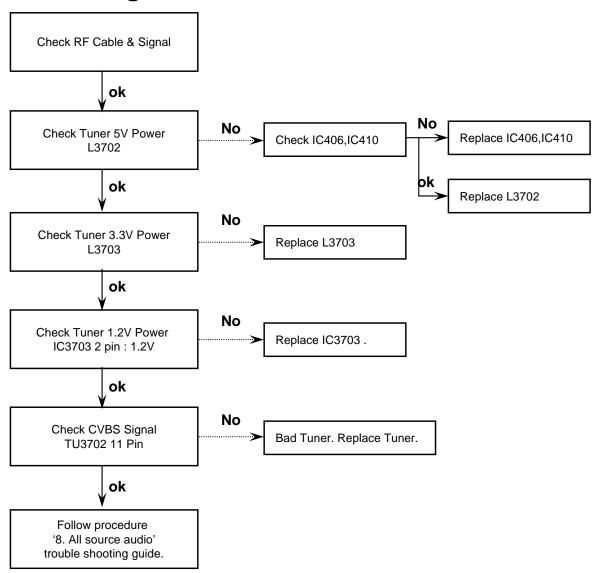
#### 8. All Source Audio



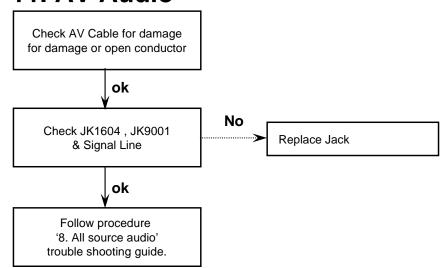
## 9. Digital TV Audio



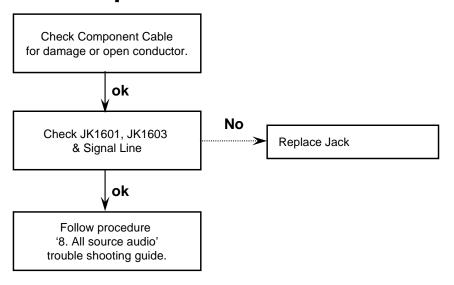
# 10. Analog TV Audio



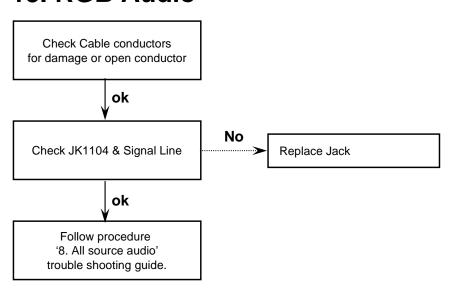
### 11. AV Audio



## 12. Component Audio



#### 13. RGB Audio



# GP3 Carry Over 모델 Block Diagram



